Schedule Grid View

Sunday, March 17, 2019

07:30AM - 10:00AM *SJCC Street Level*	**Registration Open**		
09:00AM - 05:00PM DLI Workshops *SJCC Lower Level*	Instructor-Led Workshop: Deep Learning for Robotics Dana Sheahen (NVIDIA, Curriculum Developer, Deep Learning Institute) *SJCC Room LL21C (Lower Level)*	Instructor-Led Workshop: Fundamentals of Deep Learning for Multi-GPUs Adam Grzywaczewski (NVIDIA, Deep Learning Solutions Architect) *SJCC Room LL20A (Lower Level)*	Instructor-Led Workshop: Fundamentals of Accelerated Computing with CUDA Python Robert Crovella (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*
	Instructor-Led Workshop: Fundamentals of Deep Learning for Computer Vision Alex Qi (NVIDIA, Solutions Architect) *SJCC Room LL21A (Lower Level)*	Instructor-Led Workshop: Fundamentals of Deep Learning for Natural Language Processing Yuval Mazor (NVIDIA, Senior Solutions Architect) *SJCC Room LL21D (Lower Level)*	Instructor-Led Workshop: Deep Learning for Intelligent Video Analytics Kushan Ahmadian (NVIDIA, Curriculum Developer, Deep Learning Institute) *SJCC Room LL21E (Lower Level)*
11:30AM - 01:30PM *SJCC Street Level*	**Lunch for DLI Workshops**		
04:00PM - 07:00PM *SJCC Street Level*	**Registration Open**		
05:30PM - 07:30PM Special Event	Welcome Reception *Registration (Street Level)*		
07:30PM - 09:30PM	**Dinner with Strangers**		

Monday, March 18, 2019

07:30AM - 06:00PM *SJCC Street Level*	**Registration Open**		
09:00AM - 10:30AM Tutorials	Role of Tensors in Machine Learning Anima Anandkumar (NVIDIA, Director of ML Research) *SJCC Room 210A (Concourse Level)*	Quantized Neural Networks and QEngine Yifan Zhang (Institute of Automation, Chinese Academy of Sciences, Associate Professor) *SJCC Room 210E (Concourse Level)*	Toward Open-Domain Conversational AI Yun-Nung Chen (National Taiwan University, Assistant Professor) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*
	Structural Sparsity: Speeding Up Training and Inference of Neural Networks by Linear Algorithms Matthijs Van Keirsbilck (NVIDIA, Deep Learning Research Engineer) Xiaodong Yang (NVIDIA, Senior Research Scientist)	Preparing Whole Slide Imaging Data for Deep Learning with Slideslicer Package Andrew Bishara (University of California, San Francisco, Postdoctoral Fellow in Deep Learning and Anesthesiology Clinical	Performance Analysis for Large-Scale GPU-Accelerated Applications and DL Frameworks Robert Henschel (Indiana University, Director, Science Community Tools) Guido Juckeland (Helmholtz-Zentrum Dresden-Rossendorf,

	Alexander Keller (NVIDIA, Director of Research) *SJCC Room 211A (Concourse Level)*	Instructor) Dmytro Lituiev (UCSF, Postdoctoral Research Fellow) *SJCC Room 220B (Concourse Level)*	Head of Computational Science Department) *SJCC Room 210F (Concourse Level)*
	Zero to GPU Hero with OpenACC Jeff Larkin (NVIDIA, Senior DevTech Software Engineer) *SJCC Room 210G (Concourse Level)*	High Performance Distributed Deep Learning: A Beginner's Guide Dhabaleswar K (DK) Panda (The Ohio State University, Professor and University Distinguished Scholar) Hari Subramoni (The Ohio State University, Research Scientist) Ammar Ahmad Awan (The Ohio State University, Graduate Student) *SJCC Room 210D (Concourse Level)*	RAPIDS: Deep Dive Into How the Platform Works Paul Mahler (NVIDIA, Senior Data Scientist) *SJCC Room 212A (Concourse Level)*
09:00AM - 11:00AM Instructor-Led Training *SJCC Lower Level*	Accelerating Applications with CUDA C/C++ Robert Crovella (NVIDIA, Solutions Architect) *SJCC Room LL20A (Lower Level)*	Image Classification with DIGITS David Williams (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*	Image Segmentation with TensorFlow Jonathan Bentz (NVIDIA, Solutions Architect) *SJCC Room LL21A (Lower Level)*
	Training: Dive Deep into GPU-Accelerated Investment Selection with Deep Learning Mark Bennett (University of Iowa, Department of Management Sciences, Adjunct Lecturer) *SJCC Room LL21D (Lower Level)*	Jetson Developer Tools Training Lab Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) *SJCC Room LL21C (Lower Level)*	
09:00AM - 10:00AM Sessions	Deep Learning and Beyond Will Ramey (NVIDIA, Senior Director of Developer Programs and Deep Learning Institute) *SJCC Room 220C (Concourse Level)*	Creating AI Work Groups Within the Enterprise: Developers Share Their Best Practices Markus Weber (NVIDIA, Senior Product Manager) Michael Balint (NVIDIA, Senior Product Manager) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	AI Innovation Success Stories in Retail and Consumer Products Industries Scott Brubaker (NVIDIA, Regional Manager, Retail & CPG) Paul Hendricks (NVIDIA, Solution Architect - Retail) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*
	Accelerating the Next Generation of Seismic Interpretation Yunzhi Shi (The University of Texas at Austin,	Augmented Material Creation with Substance Alchemist and RTX Real-Time Inference Rosalie Martin	Industrial AI: Probabilistic Physics-infused Deep Learning Applications Mahadevan Balasubramaniam (BHGE

Graduate Research Assistant) *Hilton Hotel Market Room (Street Level)*	(Allegorithmic, Senior Software Engineer) Baptiste Manteau (Allegorithmic, Substance Alchemist Product Owner) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	- Digital, Principal Data Scientist) Arun Subramaniyan (BHGE - Digital, Vice President Data Science & Analytics) *SJCC Room 210B (Concourse Level)*
Sharing Physically Based Materials between Renderers with MDL Lutz Kettner (NVIDIA, Director, Rendering Software and Material Definition) Jan Jordan (NVIDIA, Software Product Manager MDL) *SJCC Room 230B (Concourse Level)*	How to Create a Super Resolution Compositor that Scales to 32 Displays Thomas True (NVIDIA, Senior Applied Engineer, Professional Video and Image Processing) *SJCC Room 230C (Concourse Level)*	Towards Weakly Supervised Scene Understanding with Deep Latent Variable Models and Structured Priors Zhiding Yu (NVIDIA, Research Scientist) *SJCC Room 210C (Concourse Level)*
Scaled Speech and Language Technology in the Contact Center Wonkyum Lee (Gridspace, Lead Speech Engineer) Anthony Scodary (Gridspace, Co-Founder) Alex Barron (Gridspace, Machine Learning Engineer) *SJCC Room 212B (Concourse Level)*	The Future of Virtual Reality: Visual Perception, Adaptive Rendering, and Robotics Rachel Albert (NVIDIA, Research Scientist) Martina Sourada (NVIDIA, Senior Director, SWQA Test/Tools Development) Rochelle Pereira (NVIDIA, Senior Software Engineering Manager) Alisha Seam (AT&T Foundry | Palo Alto, Principal Engineer; Edge Computing Zone Lead) Lisa Bell-Cabrera (NVIDIA, Director Business Development VR) Claire Delaunay (NVIDIA, Vice President of Engineering) *SJCC Room 230A (Concourse Level)*	Human-Centered Autonomy Lex Fridman (MIT, Research Scientist) *SJCC Room 220A (Concourse Level)*
Deploying NVIDIA vGPU with Red Hat Virtualization (RHV) Sal Lopez (Red Hat, Solutions Architect) Shailesh Deshmukh (NVIDIA, Sr. Solutions Architect) Konstantin Cvetanov (NVIDIA, Sr. Solution Architect) *Hilton Hotel Almaden 1 Room (Street Level)*	Sensing Technologies for an Autonomous Tomorrow (Presented by Analog Devices) Stewart Sellars (Analog Devices, GM, LIDAR) *Hilton Hotel Almaden 2 Room (Street Level)*	Modeling Stellar Explosions and Our Elemental Origins With Summit Bronson Messer (ORNL, Senior Scientist) *SJCC Room 211B (Concourse Level)*

10:00AM - 11:00AM Sessions	Red Hat and the NVIDIA DGX: Tried, Tested, Trusted Jeremy Eder (Red Hat, Senior Principal Software Engineer) Andre Beausoleil (Red Hat, Inc., Senior Principal Partner Manager) *SJCC Room 212B (Concourse Level)*	Video-Based Activity Forecasting for Construction Safety Monitoring Use Cases Shuai Tang (University of Illinois at Urbana-Champaign, Graduate Research Assistant) *Hilton Hotel Almaden 2 Room (Street Level)*	The Steady State: Reduce Spikiness from GPU uUtilization with MXNet Cyrus Vahid (Amazon Web Services, Principal Evangelist - AWS AI Labs) *SJCC Room 231 (Concourse Level)*
	Fast AI Data Pre-processing with NVIDIA DALI Michał Zientkiewicz (NVIDIA, Senior Deep Learning Engineer) Janusz Lisiecki (NVIDIA, Deep Learning Manager) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	AI Vision for the Future of Retail Jordan Fisher (Standard Cognition, CEO) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	From Deep Learning to Next-Gen Visualization: A GPU-Powered Digital Transformation Ingrid Tobar (Anadarko Petroleum Corporation, Sr. Data Scientist) Amit Vij (Kinetica, President & Co-Founder) *Hilton Hotel Market Room (Street Level)*
	Irradiance Fields: RTX Diffuse Global Illumination for Local and Cloud Graphics Morgan McGuire (NVIDIA, Distinguished Research Scientist) Matt Pharr (NVIDIA,) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Real-Time Ray Tracing on Professional Head-Mounted Displays with NVIDIA RTX Jan Wurster (ESI, Solution and Technology Expert) Andreas Dietrich (ESI Group, Senior Software Developer Visualization) *SJCC Room 230A (Concourse Level)*	Integrating the NVIDIA Material Definition Language MDL in Your Application Lutz Kettner (NVIDIA, Director, Rendering Software and Material Definition) *SJCC Room 230B (Concourse Level)*
	Beyond Supervised Driving Sudeep Pillai (Toyota Research Institute, Machine Learning Research Scientist) Adrien Gaidon (Toyota Research Institute (TRI), ML Lead) *SJCC Room 220A (Concourse Level)*	Nucleus: Eight-GPU Platform For Visual Simulation David Morgan (Aechelon Technology, Principal Engineer) *SJCC Room 230C (Concourse Level)*	Deep Learning Framework for Diagnostics and Patient-Specific Design of Bioprosthetic Heart Valves Adarsh Krishnamurthy (Iowa State University, Assistant Professor) Aditya Balu (Iowa State University, Graduate Research Assistant) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
	Unsupervised Learning of Depth, Odometry, Flow and Segmentation using Competitive Collaboration Jonas Wulff (MIT, Postdoctoral Researcher) Anurag Ranjan (Max Planck Institute for Intelligent Systems, PhD Student) *SJCC Room 210C*	AI Deployment in Manufacturing: Deep Learning Visual Inspection to Improve Productivity Keisuke Fujita (Musashi Seimitsu Industries, AI Project Co-Founder) Shingo Fukui (Musashi Seimitsu Industry, AI Engineer) *SJCC Room 210B*	Modernize Digital Workspace to Meet the Modern Workforce Demand Jared Cowart (NVIDIA, Product Manager) James Hsu (Citrix, Technical Alliance Director) *Hilton Hotel Almaden 1 Room (Street Level)*

	(Concourse Level)	*(Concourse Level)*	
	An In-depth Study of HBM (Presented by SK hynix) Nayoung Lee (SK hynix, Senior DRAM Technical Marketing Manager) Changyong Ahn (SK hynix, Manager) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Accelerating our Understanding of the Nuclear Physics and the Early Universe André Walker-Loud (Lawrence Berkeley National Laboratory, Staff Scientist) *SJCC Room 211B (Concourse Level)*	
10:30AM - 11:50AM Tutorials	Deep Learning on Mobile Anirudh Koul (Aira, Head of AI) Meher Anand Kasam (Square, Software Engineer) Siddha Ganju (NVIDIA, Solution's Architect) *SJCC Room 210A (Concourse Level)*	A Trip Through the NGC TensorFlow Container Scott Ellis (NVIDIA, Solutions Architect Manager) Jeff Weiss (NVIDIA, Solutions Architect Manager) Alec Gunny (NVIDIA, Solutions Architect) *SJCC Room 210E (Concourse Level)*	Generative Adversarial Network and its Applications to Human Language Processing Hung-Yi Lee (National Taiwan University, Assistant Professor) *SJCC Room 210D (Concourse Level)*
	Using the Data You Collect: Accelerating Cybersecurity Applications with RAPIDS Bianca Rhodes (NVIDIA, Senior Full Stack Engineer) Bartley Richardson (NVIDIA, Senior Data Scientist (AI Infrastructure)) *SJCC Room 212A (Concourse Level)*		
11:00AM - 12:00PM Sessions	Accelerating Data Access for Deep Learning on Large-Scale and High-Bandwidth Distributed GPU Systems Steven Eliuk (IBM, Vice President, Deep Learning, IBM Global Chief Data Office) Seetharami Seelam (IBM T. J. Watson Research Center, Research Staff Member) *SJCC Room 212B (Concourse Level)*	Benefits of Behavior Analytics at the Edge for the Retail Market Eric Bueman (Motionloft, VP of Sales) Yating Jing (Motionloft, CV Software Engineer) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Nutanix AHV with NVIDIA Virtual GPU Solutions Ready to Meet Demands of Any Workload Tanuja Ingale (Nutanix, Sr. Product Manager, AHV, Virtualization and Management) Malcolm Crossley (Nutanix, Staff Engineer) *Hilton Hotel Almaden 1 Room (Street Level)*
	How GPU Computing Can Accelerate the Treatment of Neurological Disorders	Taming the Deep Learning Workflow Evan Sparks (Determined	Problems Taking AI to Production, and How to Fix Them!

Anthony Costa (Mount Sinai Health System, Director of Sinai Biodesign) Eric Oermann (Icahn School of Medicine at Mount Sinai, Director of AISINAI) *SJCC Room 220B (Concourse Level)*	AI, CEO) *SJCC Room 231 (Concourse Level)*	Jim Scott (MapR Technologies, VP, Enterprise Architecture) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*
Advanced Technologies and Techniques for Debugging CUDA GPU HPC Applications Nikolay Piskun (Rogue Wave Software, Director of Continuing Engineering) *SJCC Room 210F (Concourse Level)*	A New PBR Material Serving Mobile, Web, Real-Time Engines and Ray Tracing Marc Ellens (XRite, Senior Research Scientist) Jan Jordan (NVIDIA, Software Product Manager MDL) Pierre Maheut (Allegorithmic, Market Strategy Director for Architecture and Industrial Design) Jan Meseth (Dassault Systemes, Technology Stellar Precise R&D Development Director) *Hilton Hotel Almaden 2 Room (Street Level)*	Robust Learning of Multispectral Data for Geological Sample Classification Weichang Li (Aramco Research Center - Houston, Engineering Consultant) *Hilton Hotel Market Room (Street Level)*
AI from Edge to Cloud: How HPE and Seagate deliver Quality and Efficiency across the Manufacturing Supply Chain Bruce King (SEAGATE TECHNOLOGY, Data Science Technologist) Bharath Ramesh (Hewlett Packard Enterprise, Head - WW IoT Product Management & Marketing) *SJCC Room 210B (Concourse Level)*	Fast Neural Network Inference with TensorRT on Autonomous Vehicles Jeff Pyke (Zoox, Software Engineer, Machine Learning Infrastructure) Zejia Zheng (Zoox, Software Engineer) Josh Park (NVIDIA, Sr. Solutions Architect) *SJCC Room 220A (Concourse Level)*	Low-Ordered Unstructured Finite Elements Earthquake Simulation with AI and Transprecision Computing Tsuyoshi Ichimura (The University of Tokyo, Professor at Earthquake Research Institute & Department of Civil Engin) Takuma Yamaguchi (The University of Tokyo, Ph.D. Student) Kohei Fujita (The University of Tokyo, Assistant Professor) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
Bringing Personal Robots Home: Integrating Computer Vision and Human-Robot Interaction for Real-World Applications Jun Hatori (Preferred Networks, Inc., Software Engineer)	Designing a Real-Time Video Mixer for Rugged Military Deployment Chad Augustine (Curtiss-Wright, Product Manager - Integrated Systems) Greg Maynard (WOLF Advanced Technology, Chief Technology Officer)	No Compromise: Using Unified Memory for Full-Resolution Medical Image AI Joe Yeh (aetherAI, CEO) *SJCC Room 210G (Concourse Level)*

	SJCC Room 210C (Concourse Level)	*SJCC Room 230C (Concourse Level)*	
	AI+VR: The Future of Data Analytics Ciro Donalek (Virtualitics Inc, CTO, Co-Founder) Aakash Indurkhya (Virtualitics, Head of Machine Learning Projects) *SJCC Room 230A (Concourse Level)*	Design GPU Servers for Diverse Scenarios, Hyperscalers, OCP, and Edge Computing (Presented by Inspur) Alan Chang (Inspur, Sr. Director of Server Product Line) *SJCC Room 211A (Concourse Level)*	NVIDIA Optical Flow SDK in Turing GPUs Abhijit Patait (NVIDIA, Director, Multimedia Software, NVIDIA) *SJCC Room 230B (Concourse Level)*
	It Just Works: Ray-Traced Reflections in "Battlefield V" Jan Schmid (EA DICE AB, Sr. Software Engineer) Yasin Uludag (DICE, Software Engineer) Johannes Deligiannis (EA DICE, Software Engineer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Accelerating Big Data & HPC Workloads on the Cloud (Presented by Oracle) Taylor Newill (Oracle Cloud Infrastructure, Principal Product Manager) Karan Batta (Oracle Cloud Infrastructure, Principal Product Lead) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Exascale Biology: Supercomputing and explainable-AI as a Discovery Engine for Complex Biological Systems Dan Jacobson (ORNL, Chief Scientist for Computational Systems Biology) *SJCC Room 211B (Concourse Level)*
11:00AM - 01:00PM *SJCC South Hall*	**Lunch**		
11:30AM - 01:30PM Instructor-Led Training *SJCC Lower Level*	Introduction to CUDA Python with Numba Robert Crovella (NVIDIA, Solutions Architect) *SJCC Room LL20A (Lower Level)*	Introduction to Object Detection with TensorFlow Jon Howe (NVIDIA, Senior Solutions Architect) *SJCC Room LL20C (Lower Level)*	GPU-Accelerated Forecasting of Mortgage Default Risk with RAPIDS and XGBoost Matt Dancho (Business Science, CEO) Steven Thornton (RN Financial Corporation, Data Scientist) *SJCC Room LL21D (Lower Level)*
	Deep Learning Optimization and Deployment of TensorFlow Models with TensorRT Cristiana Dinea (NVIDIA, DLI Master Instructor) *SJCC Room LL21A (Lower Level)*	CUDA Programming in Python with Numba and CuPy Siu Kwan Lam (Anaconda, Software Engineer) Stanley Seibert (Anaconda, Inc, Director of Community Innovation) *SJCC Room LL21E (Lower Level)*	Jetson Developer Tools Training Lab Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) *SJCC Room LL21C (Lower Level)*
12:30PM - 02:00PM *SJCC Street Level*	**Shuttles to Keynote**		
02:00PM - 04:00PM Keynote	Opening Keynote Jensen Huang (NVIDIA, Founder & CEO)		

	San Jose State University Event Center		
06:00PM - 08:00PM Special Event	Posters & Beer *SJCC Upper Concourse*		
08:00PM - 10:00PM	**Dinner with Strangers**		

Tuesday, March 19, 2019

07:30AM - 09:00AM Special Event	Women in Deep Learning & AI Networking Breakfast and Panel *Fairmont Hotel Club Regent Room (Street Level)*		
07:30AM - 06:00PM *SJCC Street Level*	**Registration Open**		
08:00AM - 10:00AM Instructor-Led Training *SJCC Lower Level*	Deep Learning Workflows with TensorFlow, MXNet and NVIDIA Docker Gunter Roeth (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*	Signal Processing with DIGITS Adam Thompson (NVIDIA, Senior Solutions Architect) *SJCC Room LL21D (Lower Level)*	Jetson 101: Deep Learning Workflow with DIGITS and TensorRT Vincent Nguyen (NVIDIA, DLI Certified Instructor) *SJCC Room LL21C (Lower Level)*
	Deep Neural Network Training Using Images Rendered from Virtual Scenes Jakub Pietrzak (Sky Engine, CTO) Kamil Szelag (Sky Engine, C++/Python Engineer) *SJCC Room LL21E (Lower Level)*	Debugging and Optimizing CUDA Applications with Nsight Products on Linux Training Rafael Campana (NVIDIA, Sr. Engineering Manager, Developer Tools) *SJCC Room LL21A (Lower Level)*	
09:00AM - 10:00AM Sessions	Improving Customer Service with Deep Learning Techniques in a Multi-Touchpoint System Rajesh Munavalli (PayPal, Inc, Distinguished Data Scientist) *SJCC Room 210E (Concourse Level)*	Fast Training of Deep Neural Networks Using Brain-Generated Labels Sergey Vaisman (InnerEye, VP R&D) *SJCC Room 231 (Concourse Level)*	Imaging the City: GPU Simulation in Space and Time Nikita Pestrov (Habidatum International, Inc., Data Science Lead) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*
	Deep Learning for Semantic Search in E-commerce Somnath Banerjee (Walmart Labs, Director of Machine Learning) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Learn What's New with NVIDIA vGPU Anne Hecht (NVIDIA, Sr. Director Product Marketing) John Fanelli (NVIDIA, VP Product, Virtual GPU Technology) *Hilton Hotel Almaden 1 Room (Street Level)*	Multi-Platform Photo-Real Rendering: Utilizing NVIDIA'S MDL and Allegorithmic's Substance Suite for Product Imaging Pierre Maheut (Allegorithmic, Market Strategy Director for Architecture and Industrial Design) Ashleigh Miller (Amazon,

		3D Artist) Martin Pietras (Amazon, 3D Materials Artist) *Hilton Hotel Almaden 2 Room (Street Level)*
Discovering the Turing T4 GPU Architecture with Microbenchmarks Zhe Jia (Citadel, Senior HPC R&D Developer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	High-Performance Medicine to Go Deep Eric Topol, MD (Scripps Research Translational Institute, Founder and Director) *SJCC Room 220B (Concourse Level)*	Accelerate Your CUDA Development with Latest Debugging and Code Analysis Developer Tools Steve Ulrich (NVIDIA, Manager: Compute Debugger) Aurelien Chartier (NVIDIA, Senior Systems Software Engineer) *SJCC Room 210F (Concourse Level)*
Optimizing Facebook AI Workloads for NVIDIA GPUs Gisle Dankel (Facebook, Performance & Capacity Engineer) Lukasz Wesolowski (Facebook, Research Scientist) *SJCC Room 210D (Concourse Level)*	LLNL Sierra: Supercomputing and Science on a Mission Rob Neely (LLNL, LLNL Sierra Center of Excellence Lead and LLNL WSC Computing Program C) *SJCC Room 211A (Concourse Level)*	Discovering ACC Cancer Biomarkers Using a Purpose-Build Hypergraph Database and Link Prediction Pieter Derdeyn (Systems Imagination, Knowledge Engineer) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
Advanced Weather Information Recall with DGX-2 Tomohiro Ishibashi (Weather News, Inc., Director) Shigehisa Omatsu (dAIgnosis,Inc., CEO) *Hilton Hotel Market Room (Street Level)*	Using the Deepstream SDK for AI-Based Video Analytics Andrew Bull (NVIDIA, Senior Solutions Architect) Anudeep Nallamothu (NVIDIA, Solutions Architect) *SJCC Room 210A (Concourse Level)*	Designing Buildings in Real Time from Anywhere James Brogan (KPF, Principal / CIO) Cobus Bothma (KPF, Director - Applied Research) *SJCC Room 230A (Concourse Level)*
The Future of GPU Ray Tracing John Ison (NVIDIA, Director, M&E Partnerships) Vladimir Koylazov (Chaos Group, CTO) Jules Urbach (OTOY Inc., CEO) Luca Fascione (Weta Digital Ltd, Senior Head of Technology and Research) Panagiotis Zompolas (Redshift Render Technologies, CTO, co-founder) Adrien Herubel (Autodesk, Lead GPU Engineer) Kevin Margo (NVIDIA,)	Synthesizing High-Resolution Images Using Generative Adversarial Networks David Luebke (NVIDIA, VP, Graphics Research) *SJCC Room 230C (Concourse Level)*	Extreme Signal-Processing Performance Using Tensor Cores and Astronomical Imaging on GPUs John Romein (ASTRON (Netherlands Institute for Radio Astronomy), Senior Researcher) Bram Veenboer (ASTRON (Netherlands Institute for Radio Astronomy), Researcher) *SJCC Room 210G (Concourse Level)*

	Max Liani (Pixar, Sr. Lead Engineer) *SJCC Room 230B (Concourse Level)*		
	The NVIDIA Jetson Platform for Autonomous Machines Jesse Clayton (NVIDIA, Senior Manager, Product Management for Intelligent Machines) *SJCC Room 210B (Concourse Level)*	RAPIDS: The Platform Inside and Out Joshua Patterson (NVIDIA, Director, AI Infrastructure) *SJCC Room 212A (Concourse Level)*	An Inside Look into NVIDIA Autonomous Vehicle Drive Missions Neda Cvijetic (NVIDIA, Senior Manager, Autonomous Vehicles) *SJCC Room 220A (Concourse Level)*
	Artificial Intelligence: Technology's Inevitable Consequence Oliver Schabenberger, PhD (SAS, Executive Vice President, Chief Operating Officer & Chief Technology) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Isaac Gym Viktor Makoviichuk (NVIDIA, Lead Developer of Isaac Gym) *SJCC Room 211B (Concourse Level)*	Edge to Core: A Meta Study of Data Complexity in AI (Presented by DDN) James Coomer (DDN, Senior Vice President Products) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*
	Getting the Most from GTC and NVIDIA's Developer Program Greg Estes (NVIDIA, VP Corporate Marketing and Developer Programs) *SJCC Room 220C (Concourse Level)*	Simplifying AI, Data Science, and HPC Workloads with NGC Adel El Hallak (NVIDIA, Director of Product Management, NGC) Philip Rogers (NVIDIA, Chief Software Architect Compute Server) *SJCC Room 212B (Concourse Level)*	
10:00AM - 12:00PM Instructor-Led Training *SJCC Lower Level*	Word Generation with TensorFlow Yuval Mazor (NVIDIA, Senior Solutions Architect) *SJCC Room LL20A (Lower Level)*	Image and Video Captioning by Combining CNNs and RNNs Osama Qazi (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*	High Performance Computing Using Containers Scott McMillan (NVIDIA, Solutions Architect) Chintan Patel (NVIDIA, Manager) *SJCC Room LL21D (Lower Level)*
	Training: Distributed Deep Learning with Horovod Alex Sergeev (Uber Technologies, Inc., Sr Software Engineer II) *SJCC Room LL21E (Lower Level)*	Jetson 101: Deep Learning Workflow with DIGITS and TensorRT Vincent Nguyen (NVIDIA, DLI Certified Instructor) *SJCC Room LL21C (Lower Level)*	Debugging and Optimizing CUDA Applications with Nsight Products on Linux Training Rafael Campana (NVIDIA, Sr. Engineering Manager, Developer Tools) *SJCC Room LL21A (Lower Level)*
10:00AM - 11:00AM Sessions	How to Accelerate and Scale AI Deployment with	Semi-supervised deep learning applications	PAI Tensor Accelerator and Optimizer (PAI-Tao)

Proven Architecture Designs Ludwig Gamache (Element AI Inc., Head of IT) Charlie Boyle (NVIDIA, Senior Director, Product Management) *SJCC Room 212B (Concourse Level)*	Bryan Catanzaro (NVIDIA, VP of Applied Deep Learning Research) *SJCC Room 220C (Concourse Level)*	Jun Yang (Alibaba, Algorithm Architect) Guozhen Pan (AntFin, Staff Engineer) *SJCC Room 231 (Concourse Level)*
Augmented Reality Solution for Advanced Driver Assistance Sergii Bykov (Apostera, Technical Lead) *SJCC Room 210E (Concourse Level)*	Real-Time Computer Vision in Retail Frank Hinek (NCR, Innovation Lab Leader) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Cloud Dynamics: Supercharging Graphics and Rendering Workloads with NVIDIA Quadro Virtual Workstations Manvender Rawat (NVIDIA, Product Manager) *Hilton Hotel Almaden 1 Room (Street Level)*
Accelerating Product Visualization in KeyShot using RTX Henrik Jensen (Luxion, Chief Scientist) *Hilton Hotel Almaden 2 Room (Street Level)*	Using NVIDIA CUDF to Simplify and Accelerate Data Prep for Credit Card Algo. Prediction Weicheng (Richard) Liu (Wells Fargo Bank, Vice President) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Build Systems: Exploring Modern CMake + CUDA Robert Maynard (Kitware, Inc, Principal Engineer) *SJCC Room 210F (Concourse Level)*
Extreme Neural Network Computing Transforms Speech Quality Chris Rowen (BabbleLabs, CEO) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Getting Started with TensorFlow on GPUs Magnus Hyttsten (Google, Inc., Developer Advocate) *SJCC Room 210D (Concourse Level)*	LLNL Sierra: The Software Stack Propelling Simulations to Exascale David Beckingsale (Lawrence Livermore National Laboratory, Computer Scientist) Olga Pearce (Lawrence Livermore National Laboratory, Computer Scientist) *SJCC Room 211A (Concourse Level)*
Autoencoding Genetic Data for Disease Risk Prediction Ali Torkamani (Scripps Research Translational Institute, Associate Professor) Raquel Dias (Scripps Research Translational Institute, Lead Scientist) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Deep Learning for Improved Utilization of Satellite Data in Weather Forecasting David Hall (NVIDIA, Solution Architect) *Hilton Hotel Market Room (Street Level)*	3D Object Tracking and Localization for AI City Gaoang Wang (University of Washington, Research Assistant of University of Washington) *SJCC Room 210A (Concourse Level)*
How is NVIDIA Holodeck Changing the Architectural	Optix For Real Time Display Of Very Complex	The Journey to Immersive Storytelling

	Design Process? Hilda Espinal (CannonDesign, Chief Technology Officer) Ernesto Pacheco (CannonDesign, Director of Visualization) Andrew Schilling (CannonDesign, Chief Infrastructure Officer) *SJCC Room 230A (Concourse Level)*	Scenes Nicolas Guiard (Isotropix, Head of R&D) *SJCC Room 230B (Concourse Level)*	Vicki Dobbs Beck (ILMxLAB, Executive in Charge) *SJCC Room 230C (Concourse Level)*
	Machine Learning in Action within a Large Regional Healthcare System (Geisinger) Brandon Fornwalt (Geisinger, Associate Professor) Aalpen Patel (Geisinger, Chairman, System Radiology) *SJCC Room 220B (Concourse Level)*	Fast Convolutions Via the Overlap-and-Save Method Using Shared Memory FFT Karel Adamek (Department of Engineering Sciences, University of Oxford, Research Associate) *SJCC Room 210G (Concourse Level)*	Framing Business Problems As Machine Learning Problems Carlos Escapa (AWS, Global Lead, AI/ML Consulting Practice) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*
	cuDF: RAPIDS GPU-Accelerated Data Frame Library Keith Kraus (NVIDIA, Manager AI Infrastructure) *SJCC Room 212A (Concourse Level)*	NVIDIA DRIVE Constellation: Virtual Reality Simulation for Autonomous Vehicle Validation Zvi Greenstein (NVIDIA, General Manager) *SJCC Room 220A (Concourse Level)*	Applications of Deep Learning for Locomotion Animation Gavriel State (NVIDIA, Senior Director of Simulation and AI) *SJCC Room 211B (Concourse Level)*
	Bringing AI to the Enterprise (Presented by IBM) Sumit Gupta (IBM, VP, AI, Machine Learning & HPC) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*		
11:00AM - 12:00PM Sessions	Demystifying Deep Learning Infrastructure Choices Using MLPerf Benchmark Suite Ramesh Radhakrishnan (Dell EMC, Distinguished Engineer) Lizy John (University of Texas, Cullen Trust for Higher Education Endowed Professor) *SJCC Room 212B (Concourse Level)*	Is This The End of "Computer Says No"? Nigel Cannings (Intelligent Voice, CTO) *SJCC Room 231 (Concourse Level)*	Scaling Smart Retail: From Self-Checkout Systems to Empowering Sales Force Pradeep Pydah (Maxerience, CEO) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*
	Maximizing Utilization of NVIDIA Virtual GPUs in	Native GPU Accelerated Geometry - A New Era	Financial Services Transformation with

VMware vSphere for End-to-End Machine Learning Manvender Rawat (NVIDIA, Product Manager) Uday Kurkure (VMWare, Staff Engineer) *Hilton Hotel Almaden 1 Room (Street Level)*	Begins! Harshil Goel (Dyndrite Corporation, CEO) *Hilton Hotel Almaden 2 Room (Street Level)*	Intelligent Cognitive Jianzong Wang (Ping An Technology (ShenZhen), Deputy Chief Engineer) Han Mei (PingAn Technology, US Research Labs, Director) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*
AI and Machine Learning in Radiology: A Reality Check Paul Chang (University of Chicago School of Medicine, Professor & Vice Chairman) *SJCC Room 220B (Concourse Level)*	An Introduction to TensorFlow 2.0 Paige Bailey (Google, TensorFlow Developer Advocate) *SJCC Room 210D (Concourse Level)*	LLNL Sierra: Lessons Learned from Porting LLNL Production Applications to Sierra David Dawson (Lawrence Livermore National Laboratory, Computational Physicist) *SJCC Room 211A (Concourse Level)*
Machine Learning Parameterizations of Atmospheric Processes David Gagne (National Center for Atmospheric Research, Machine Learning Scientist) *Hilton Hotel Market Room (Street Level)*	Real-Time Automatic Incident Detection Inside Tunnels Based on Volta Architecture Amir Nakib (Vinci Autoroutes, AI Research Head) *SJCC Room 210A (Concourse Level)*	XR Strategies for Engineering and Construction Dave Tyner (Autodesk, Inc., Thought Leadership Program Manager, Autodesk Construction) Alberto Arenas (Autodesk, Senior AR/VR Service Line Manager) *SJCC Room 230A (Concourse Level)*
From Production to Real-Time Ray Tracing with V-Ray GPU and Project Lavina Phillip Miller (Chaos Group, Vice President of Product Management) Alexander Soklev (Chaos Group, Software Developer) Vladimir Koylazov (Chaos Group, CTO) *SJCC Room 230B (Concourse Level)*	Performance Analysis of GPU-Accelerated Applications using the Roofline Model Samuel Williams (Lawrence Berkeley National Laboratory, Staff Scientist) Charlene Yang (Lawrence Berkeley National Laboratory, Application Performance Consultant) *SJCC Room 210G (Concourse Level)*	Deep Learning Institute Executive Workshop Stephen Piron (Dessa, co-founder) Will Ramey (NVIDIA, Senior Director of Developer Programs and Deep Learning Institute) Tim Delisle (Datalogue, CEO) Jeff Goldman (Procter and Gamble, Director of Enterprise Data Science) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*
Postmates' Serve: Making Sidewalk Robots that Care Nic Fischer (Postmates X, System and Robotics Lead) *SJCC Room 210B (Concourse Level)*	Embody: Reclaiming the Body's Potential inside the Digital Landscape Thomas Wester (MAP + Glowbox, Creative Technologist) Melissa Painter (MAP Design Lab, Founder, Creative Director) *SJCC Room 230C (Concourse Level)*	Dask Extensions and New Developments with RAPIDS Matt Rocklin (NVIDIA, Senior Data Engineer) *SJCC Room 212A (Concourse Level)*

	Understanding Deep Networks through Properties of the Input Space Sebastian Palacio (German Research Center for Artificial Intelligence (DFKI), Ph.D. Researcher) *SJCC Room 210E (Concourse Level)*	Context-Aware Synthesis of Object Instance in 2D and 3D Scenes Xueting Li (University of California, Merced, Ph.D. student) Sifei Liu (NVIDIA, Data-Driven Affordance Learning in 2D and 3D Scene) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Using ONNX for Accelerated Inferencing on Cloud and Edge Prasanth Pulavarthi (Microsoft, Principal PM, AI Frameworks) Kevin Chen (NVIDIA, Deep Learning Software Engineer) *SJCC Room 220C (Concourse Level)*
	Deep Domain Adaptation and Generative Models for Single Cell Genomics Gerald Quon (University of California, Davis, Assistant Professor) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Ray-Traced Global Illumination for Games: Massively Parallel Path Space Filtering Alexander Keller (NVIDIA, Director of Research) Nikolaus Binder (NVIDIA, Research Scientist) *SJCC Room 211B (Concourse Level)*	Profiling Deep Learning Networks Poonam Chitale (NVIDIA, Senior Product Manager) David Zier (NVIDIA, SW Engineering Manager) *SJCC Room 210F (Concourse Level)*
	AI Growing Pains: Platform Considerations for Moving from POC to Large-Scale Deployments Saikumar Devulapalli (Dell Technologies, Global Head of Data Analytics Platform Portfolio and GTM) Claudio Fahey (Dell Technologies, Chief Solutions Architect, Artificial Intelligence and Analytics) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Accelerating AVs - AI in Generative Design and Simulation Danny Atsmon (Cognata Ltd., Founder & CEO) Simon Berard (Dassault Systèmes, Senior Strategic Planning Analyst) *SJCC Room 220A (Concourse Level)*	
12:00PM - 07:00PM *SJCC Expo Hall 1-3*	**Exhibits**		
12:00PM - 02:00PM *SJCC South Hall*	**Lunch**		
01:00PM - 02:00PM Sessions	Deploying AI on Jetson Xavier/DRIVE Xavier with TensorRT and MATLAB Jaya Shankar (MathWorks, Principal Software Engineer) Avinash Nehemiah (MathWorks, Product Manager) *SJCC Room 210E (Concourse Level)*	Distributed TensorFlow with Distribution Strategies Magnus Hyttsten (Google, Inc., Developer Advocate) *SJCC Room 210D (Concourse Level)*	Accelerate Your Speech Recognition Pipeline on the GPU Justin Luitjens (NVIDIA, Senior Developer Technologies Engineer) Hugo Braun (NVIDIA, AI Developer Technology Engineer) *SJCC Room 231 (Concourse Level)*
	Deep Hyperspherical	Containers Democratize	Powering Intelligent Video

Embedding and Decoupled Learning for Visual Recognition Weiyang Liu (Georgia Institute of Technology, PhD Candidate) Zhen Liu (Georgia Institute of Technology, Graduate Student) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	HPC CJ Newburn (NVIDIA, Principal Architect for HPC, NVIDIA Compute Software) *SJCC Room 212B (Concourse Level)*	Analytics with NVIDIA Virtual GPU Vinay Bagade (NVIDIA, Performance Engineering) *Hilton Hotel Almaden 1 Room (Street Level)*
How GPUs, Real-Time and Virtual Reality Enriched the Building Design Process at Perkins+Will Dan Chasteen (Perkins+Will, Digital Practice Manager) *Hilton Hotel Almaden 2 Room (Street Level)*	Using AI Machine Learning to Explore Large Streaming Financial Data Sets to Improve Market Making Peter Decrem (Citi, Director) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Frontiers of AI in Medicine: Overcoming Current Challenges and Moving Beyond Classification Imon Banerjee (Stanford University, Instructor) Daniel Rubin (Stanford University, Professor of Biomedical Data Science, Radiology, and Medicine) *SJCC Room 220B (Concourse Level)*
CUDA Kernel Profiling Using NVIDIA Nsight Compute Magnus Strengert (NVIDIA, Sr. Software Engineer) Sanjiv Satoor (NVIDIA, Sr. Engineering Manager, Developer Tools) *SJCC Room 210F (Concourse Level)*	Intelligent Product Image System Zheng ZhiTong (JD.com, Chief Researcher) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	CUDA: New Features and Beyond Stephen Jones (NVIDIA, Principal Software Engineer) *SJCC Room 220C (Concourse Level)*
Amber18: An Enhanced Molecular Simulations Program for Studying Biopolymers and Dissecting Ligand Binding Energies Taisung Lee (Rutgers, the State University, Research Professor) David Cerutti (Rutgers, the State University, Research Professor) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Porting MURaM (Max Planck University of Chicago Radiative MHD) to GPUs Using OpenACC Eric Wright (University of Delaware, Student) Sunita Chandrasekaran (University of Delaware, Assistant Professor) Richard Loft (National Center for Atmospheric Research, Director, Technology Development) *Hilton Hotel Market Room (Street Level)*	How AI-Driven Media Analytics Enable Novel Discovery and Navigational Experiences Jan Neumann (Comcast Applied AI,) *SJCC Room 210A (Concourse Level)*
The Future of GPU Rendering: Real-Time Raytracing, Holographic Displays and Light Field Media Jules Urbach (OTOY Inc., CEO)	Efficient Distributed Storage I/O using NVMe and GPUDirect in a PCIe Network Jonas Markussen (Dolphin Interconnect Solutions, Senior Software Engineer)	Distributed Training and Fast inter-GPU Communication with NCCL Sylvain Jeaugey (NVIDIA, Senior Software Engineer) *SJCC Room 211A (Concourse Level)*

SJCC Room 230B (Concourse Level)	*SJCC Room 210G (Concourse Level)*	
Genesis: Real-Time Raytracing in Virtual Production Francesco Giordana (MPC Film, Realtime Software Architect) Marco Giordano (MPC Film, Senior Developer) *SJCC Room 230C (Concourse Level)*	How Walmart Improves Forecast Accuracy with NVIDIA GPUs John Bowman (Walmart, Director of Data Science at Walmart Lab) *SJCC Room 212A (Concourse Level)*	Real-Time Inference: Considerations for Achieving Best Performance in Applications and Games Don Brittain (NVIDIA, Principal Engineer) *SJCC Room 211B (Concourse Level)*
Connect with the Experts: NVIDIA Transfer Learning Toolkits for Industry Specific Solutions Poonam Chitale (NVIDIA, Senior Product Manager) Andy Feng (NVIDIA, VP Architect) Nick Becker (NVIDIA,) Alvin Ihsani (NVIDIA, Solutions Architect) Eddie Weill (NVIDIA, Solution Architect) Mahendra Roopa (NVIDIA, Sr. Product Manager) *SJCC Hall 3 Pod A (Concourse Level)*	Data Science Workstation: The Missing Link to Productivity? (Presented by Dell) David Patschke (Dell, AI/ML Strategy Director, Precision Workstations) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	How 6 River Systems Leveraged ROS and the NVIDIA Jetson Platform (TX1/TX2) to Build a Fleet of Autonomous Collaborative Robots Dan Winkler (6 River Systems, Inc., Director of Software Engineering) *SJCC Room 210B (Concourse Level)*
Autonomy for Mass Production Vehicles Junli Gu (Xiaopeng Motors, Vice President) *SJCC Room 220A (Concourse Level)*	Connect with the Experts: Deep Learning Libraries – cuDNN, cuBLAS, CUTLASS Faisal Zaghloul (NVIDIA,) Mathieu Zhang (NVIDIA,) Andrew Kerr (NVIDIA, Senior Compute Architect) Piotr Majcher (NVIDIA,) Kevin Vincent (NVIDIA,) Khairul Kabir (NVIDIA,) Slawek Stepniewski (NVIDIA,) Yang Xu (NVIDIA,) Philippe Vandermersch (NVIDIA,) *SJCC Hall 3 Pod D (Concourse Level)*	Connect with the Experts: NVIDIA NGX Technology - AI for Visual Applications Rick Grandy (NVIDIA,) Andrew Page (NVIDIA,) *SJCC Hall 3 Pod C (Concourse Level)*
Connect with the Experts: Performance Analysis and Optimization Kamesh Arumugam (NVIDIA,) Moises Hernandez (NVIDIA, -) Peng Wang (NVIDIA,) *SJCC Hall 3 Pod E (Concourse Level)*		

01:00PM - 03:00PM Instructor-Led Training *SJCC Lower Level*	Coarse to Fine Contextual Memory for Medical Imaging David Nola (NVIDIA, Deep Learning Solutions Architect) *SJCC Room LL20A (Lower Level)*	Accelerated Data Science Pipeline with RAPIDS on Azure Kaarthik Sivashanmugam (Microsoft, AI Platform) Manuel Reyes-Gomez (NVIDIA, Developer Relations Manager) *SJCC Room LL21A (Lower Level)*	Machine Learning for Anomaly Detection Eric Harper (NVIDIA, Solutions Architect) *SJCC Room LL21D (Lower Level)*
	Jetson 201: Reinforcement Learning in Robotics Dustin Franklin (NVIDIA, Sr. Technical Marketing Manager & Evangelist) *SJCC Room LL21C (Lower Level)*	F1/10 Autonomous Racing: 1/10th the Scale, 10 Times the Fun! Madhur Behl (University of Virginia, Assistant Professor) *SJCC Room LL21E (Lower Level)*	Image Style Transfer with Torch Gary Burnett (NVIDIA, Solution Architect) *SJCC Room LL20C (Lower Level)*
02:00PM - 03:00PM Sessions	End-to-End Design and Optimization for Baidu's Large-Scale and Distributed Training System Ruiquan Ding (Baidu, AI System Architect) *SJCC Room 212B (Concourse Level)*	Integration of TensorRT with DALI on Xavier Anurag Dixit (NVIDIA, Deep Learning Software Engineer - Autonomous Driving) Josh Park (NVIDIA, Sr. Solutions Architect) *SJCC Room 210E (Concourse Level)*	OpenSeq2Seq: A Deep Learning Toolkit for Speech Recognition, Speech Synthesis, and NLP Oleksii Kuchaiev (NVIDIA, Senior Applied Research Scientist (Deep Learning)) Boris Ginsburg (NVIDIA, Principal Deep Learning Engineer) *SJCC Room 231 (Concourse Level)*
	Tackling 3D ToF Artifacts through Learning and the FLAT Dataset Orazio Gallo (NVIDIA, Senior Research Scientist) Iuri Frosio (NVIDIA, Senior Research Scientist) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Nanostores: Modular Convenience Stores for Everyone, Powered by GPUs Steve Gu (AiFi, Co-Founder & CEO) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Virtual Desktops by Day, Computational Workloads by Night: An Example Infrastructure Konstantin Cvetanov (NVIDIA, Sr. Solution Architect) Shailesh Deshmukh (NVIDIA, Sr. Solutions Architect) *Hilton Hotel Almaden 1 Room (Street Level)*
	Machine Learning @ Bloomberg: Building on Kubernetes Ian Hummel (Bloomberg, Data Architecture, Office of the CTO) David Eis (Bloomberg, Senior Software Engineer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Challenges of Deploying and Validating an AI Tool into Medical Practice Safwan Halabi (Stanford University, Clinical Associate Professor of Radiology) *SJCC Room 220B (Concourse Level)*	Using GPU Power for NumPy-syntax Calculations Crissman Loomis (Preferred Networks, Business Development/Engineer) Shunta Saito (Preferred Networks, Researcher) *SJCC Room 210F (Concourse Level)*

Multi GPU Programming Models Jiri Kraus (NVIDIA, Senior Devtech Compute) *SJCC Room 220C (Concourse Level)*	Distributed Deep Learning with Horovod Alex Sergeev (Uber Technologies, Inc., Sr Software Engineer II) *SJCC Room 210D (Concourse Level)*	Bringing Gromacs Up to Speed on Modern Multi-GPU Systems Alan Gray (NVIDIA, Senior Developer Technology Engineer) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
Advancing U.S. Weather Prediction Capabilities with Exascale HPC Mark Govett (NOAA Earth System Research Laboratory, Chief, High Performance Computing Section) *Hilton Hotel Market Room (Street Level)*	Baidu Cloud AI Solution in Video Understanding Dong Wang (Baidu Inc., Principal Architect) Nie Lei (Baidu Inc., Senior Algorithm Engineer) *SJCC Room 210A (Concourse Level)*	Sixty Milliseconds To Get It Right: Gaze-Contingent Rendering and Human Perception Rachel Albert (NVIDIA, Research Scientist) *SJCC Room 230A (Concourse Level)*
Adding GPU Acceleration to Pixar Renderman Max Liani (Pixar, Sr. Lead Engineer) *SJCC Room 230B (Concourse Level)*	Autonomous Parking on NVIDIA DRIVE Sihao Ding (Volvo Car Corporation, Senior Research Engineer) Andreas Wallin (Volvo Car Corporation, Senior Manager AD and ADAS) *SJCC Room 220A (Concourse Level)*	Beyond Polygons, Voxels, and Rasterization Nejc Lesek (LightMass, CTO) *SJCC Room 230C (Concourse Level)*
Large-Scale Road Network Simulations for Smart Cities Peter Heywood (The University of Sheffield, Research Software Engineer) *SJCC Room 210G (Concourse Level)*	How to Make Your Life Easier in the Age of Exascale Computing Using NVIDIA GPUDirect Technologies Elena Agostini (NVIDIA, Software Engineer) Davide Rossetti (NVIDIA, Software Engineer) *SJCC Room 211A (Concourse Level)*	Deep Learning Implementers Panel: Experts Discuss Keys to Their Successes Norman Müller (BMW Group, Data Scientist) Tony Paikeday (NVIDIA, Director, Product Marketing) Enhao Gong (Subtle Medical, CEO) Zachary Hanif (Capital One Center for Machine Learning, Senior Director) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*
Building a GPU-Focused CI Solution Mike Wendt (NVIDIA, Engineer) *SJCC Room 212A (Concourse Level)*	"Shadows" of the Tomb Raider: Ray Tracing Deep Dive Michiel Roza (Nixxes, Programmer) Holger Gruen (NVIDIA, Senior Developer Technology Engineer) Jon Story (NVIDIA, Developer Technology Manager) *SJCC Room 211B*	Connect with the Experts: Optimizing and Deploying DL Inference Solutions on Windows Chris Hebert (NVIDIA, DevTech Software Engineer) Don Brittain (NVIDIA, Principal Engineer) Sven Middelberg (NVIDIA, Senior Developer Technology Engineer)

		(Concourse Level)	*SJCC Hall 3 Pod A (Concourse Level)*
	Connect with the Experts: OpenACC Andreas Herten (Jülich Supercomputing Centre, Researcher GPUs in High Performance Computing) Jeff Larkin (NVIDIA, Senior DevTech Software Engineer) Guido Juckeland (Helmholtz-Zentrum Dresden-Rossendorf, Head of Computational Science Department) Sunita Chandrasekaran (University of Delaware, Assistant Professor) Michael Wolfe (NVIDIA, Compiler Engineer) Mathew Colgrove (NVIDIA, PGI Devtech Engineer) *SJCC Hall 3 Pod D (Concourse Level)*	Accelerate, Scale, and Operationalize Data Pipelines (Presented by Cisco) Han Yang (Cisco, Han Yang, PhD, Cisco AI/ML Product Manager) Debo Dutta (Cisco, Distinguished Engineer) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Robotic Solutions that Scale Jean-Baptiste Passot (Brain Corp, Vice President, Platform and AI) *SJCC Room 210B (Concourse Level)*
	Connect with the Experts: CUDA & Graphics Developer Tools Sebastien Domine (NVIDIA,) Robert Knight (NVIDIA, Software Engineer) Dmitry Polyanitsa (NVIDIA,) Rafael Campana (NVIDIA, Sr. Engineering Manager, Developer Tools) Steve Ulrich (NVIDIA, Manager: Compute Debugger) *SJCC Hall 3 Pod E (Concourse Level)*	Connect with the Experts: Deep Learning for Climate and Weather David Hall (NVIDIA, Solution Architect) *SJCC Hall 3 Pod C (Concourse Level)*	Connect with the Experts: Deep Learning Training for Tensor Cores Ben Barsdell (NVIDIA,) Deyu Fu (NVIDIA,) J Jiang (NVIDIA,) Michael Carilli (NVIDIA, Senior Developer Technology Engineer) Nathan Luehr (NVIDIA, Senior Developer Technology Engineer) Carl Case (NVIDIA, Senior Architect) Sharan Chetlur (NVIDIA,) *SJCC Hall 3 Pod F (Concourse Level)*
	Smart Parking Drives Economic Revitalization (Presented by Arrow) Brian Shay (Arrow Electronics, IoT Solutions Manager) *Hilton Hotel Almaden 2 Room (Street Level)*		
03:00PM - 04:00PM Sessions	Intelligent Operation and Maintenance of Public Cloud Based on GPU-Accelerated Machine Learning Zhiming Wang (Alibaba	Neural Monkey: A Natural Language Processing Toolkit Jindřich Helcl (Charles University, Research Assistant)	GPU Acceleration of Word Embedding Models for Very Large Datasets Rajesh Bordawekar (IBM, Principal Research Staff Member)

(China), Senior Engineer) Feng Xie (Alibaba (China), Senior Engineer) *SJCC Room 212B (Concourse Level)*	*SJCC Room 231 (Concourse Level)*	*Hilton Hotel Santa Clara Room (Level 2/Concourse)*
Inference at Reduced Precision on GPUs Hao Wu (NVIDIA, Compute Architecture) *SJCC Room 210D (Concourse Level)*	TPC-H Benchmark on DGX-2: A New Paradigm for OLAP and Decision Support Richard Heyns (Brytlyt, CEO) Piotr Kowalski (Brytlyt, Senior Engineer) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Virtual GPU Powers AI and Deep Learning in Universities Konstantin Cvetanov (NVIDIA, Sr. Solution Architect) Emily Apsey (NVIDIA, Professional Visualization Performance Engineering) Neranjan Edirisinghe (Georgia State University, HPC Facilitator) John Meza (Esri, Performance Engineering Team Lead) *Hilton Hotel Almaden 1 Room (Street Level)*
The Industry That Time Forgot: Pioneering the Digital Future in Architecture Christopher Morse (SHoP Architects, Associate, Interactive Visualization) Geoffrey Bell (SHoP Architects PC, Associate) *Hilton Hotel Almaden 2 Room (Street Level)*	Deep Learning Extraction for Counterparty Risk Signals from a Corpus of Millions of Documents Moody Hadi (S&P Global - Market Intelligence - Risk Services, Group Manager R&D and Innovation) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Developing a Roadmap for Machine Learning in Clinical Radiology Christopher Hess (University of California, San Francisco, Professor and Chair, Radiology & Biomedical Imaging) *SJCC Room 220B (Concourse Level)*
Fast and Accurate Object Detection with PyTorch and TensorRT Floris Chabert (NVIDIA, Solutions Architect) Prethvi Kashinkunti (NVIDIA, Solutions Architect) *SJCC Room 210E (Concourse Level)*	OpenACC-Based GPU Acceleration of Chemical Shift Prediction Sunita Chandrasekaran (University of Delaware, Assistant Professor) Eric Wright (University of Delaware, Student) Alex Bryer (University of Delaware, Graduate Research Assistant) Juan Perilla (University of Delaware, Assistant Professor) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Combining Machine Learning and GPU Acceleration to Transform Atmospheric Science Richard Loft (National Center for Atmospheric Research, Director, Technology Development) *Hilton Hotel Market Room (Street Level)*
A Unique Insight into Logistics Neo Song (SF Technology, Chief Engineer) *SJCC Room 210A (Concourse Level)*	NVGaze: Anatomy-Aware Augmentation for Low-Latency, Near-Eye Gaze Estimation Joohwan Kim (NVIDIA, Research Scientist) Michael Stengel (NVIDIA, Research Scientist)	Autonomous Driving: The Good, The Bad and The Ugly Xiaodi Hou (TuSimple, Founder, President & CTO) *SJCC Room 220A (Concourse Level)*

	SJCC Room 230A (Concourse Level)	
Efficient Hybrid Eulerian-Lagrangian Simulation on the GPU Kui Wu (University of Utah, Ph.D. Student) *SJCC Room 230C (Concourse Level)*	MVAPICH2-GDR: High-Performance and Scalable CUDA-Aware MPI Library for HPC and AI Dhabaleswar K (DK) Panda (The Ohio State University, Professor and University Distinguished Scholar) Hari Subramoni (The Ohio State University, Research Scientist) *SJCC Room 211A (Concourse Level)*	KiwiBots: Using the Power of GPUs to Solve the Last-Mile Delivery Problem Carlos Alvarez (Kiwi Campus, AI Engineer Lead) David Cardozo (Kiwi Campus, Machine Learning Engineer) *SJCC Room 210B (Concourse Level)*
Bringing the Arnold Renderer to the GPU Adrien Herubel (Autodesk, Lead GPU Engineer) *SJCC Room 230B (Concourse Level)*	Connect with the Experts: Fast Data Pre-Processing with NVIDIA Data Loading Library (DALI) Przemek Tredak (NVIDIA, Senior Developer Technology Engineer) Michał Zientkiewicz (NVIDIA, Senior Deep Learning Engineer) Janusz Lisiecki (NVIDIA, Deep Learning Manager) *SJCC Hall 3 Pod E (Concourse Level)*	Memory Management on Modern GPU Architectures Nikolay Sakharnykh (NVIDIA, Sr. Developer Technology Engineer) *SJCC Room 210G (Concourse Level)*
Connect with the Experts: Multi-GPU Programming Akshay Venkatesh (NVIDIA, Senior Software Engineer) Jiri Kraus (NVIDIA, Senior Devtech Compute) *SJCC Hall 3 Pod C (Concourse Level)*	I am AI: How Humans and Technology are Working Together Ron Alfa (Recursion, Senior Vice President, Translational Discovery,) Juan Bravo (Agrobot, CEO & founder) Noah Kravitz (AI Postcast, Host) Christian Thurow (Searidge Technologies, VP of Software Solutions) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Accelerate Innovation in the Enterprise with Distributed ML / DL on GPUs (Presented by HPE) Thomas Phelan (BlueData (recently acquired by HPE), Co-Founder) Nanda Vijaydev (BlueData (recently acquired by HPE), Lead Data Scientist) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*
Featured Talk: Accelerating AI Bill Dally (NVIDIA, Chief Scientist) *SJCC Room 220C (Concourse Level)*	Connect with the Experts: Deep Learning Basics Boris Fomitchev (NVIDIA,) Triston Cao (NVIDIA,) Sami Kama (NVIDIA,) Sergei Nikolaev (NVIDIA,) Dick Carter (NVIDIA,) Ben Barsdell (NVIDIA,) J Jiang (NVIDIA,) Cliff Woolley (NVIDIA,) Peter Pyun (NVIDIA,	Real-Time Path Tracing and Denoising in 'Quake 2' Alexey Panteleev (NVIDIA, Principal Devtech Engineer) Christoph Schied (Karlsruhe Institute of Technology, PhD Student) *SJCC Room 211B (Concourse Level)*

		Principal Solution Architect) Pooya Davoodi (NVIDIA, Senior Software Engineer) Kaixi Hou (NVIDIA,) Michael O'Connor (NVIDIA, Director of Software, Deep Learning Optimized Frameworks) Ofir Zamir (NVIDIA,) Syed Ahmed (NVIDIA,) Adam Tetelman (NVIDIA,) Andrei Ivanov (NVIDIA,) *SJCC Hall 3 Pod A (Concourse Level)*	
03:00PM - 05:00PM Instructor-Led Training *SJCC Lower Level*	Accelerating Data Science Workflows with RAPIDS Raghav Mani (NVIDIA, Developer Relations Manager) *SJCC Room LL21A (Lower Level)*	Deep Learning for Anomaly Detection Eric Harper (NVIDIA, Solutions Architect) *SJCC Room LL21D (Lower Level)*	Jetson 201: Reinforcement Learning in Robotics Dustin Franklin (NVIDIA, Sr. Technical Marketing Manager & Evangelist) *SJCC Room LL21C (Lower Level)*
	Data Augmentation and Segmentation with Generative Networks for Medical Imaging David Nola (NVIDIA, Deep Learning Solutions Architect) *SJCC Room LL20A (Lower Level)*		
04:00PM - 05:00PM Sessions	ImageNet in 18 Minutes Using Public Cloud Yaroslav Bulatov (South Park Commons, Researcher) *SJCC Room 212B (Concourse Level)*	Recent Advances of PaddlePaddle Xi Chen (Baidu, AI Platform Research Engineer) *SJCC Room 210E (Concourse Level)*	Accelerate Time Series Databases with GPUs and Machine Learning Xinyang Yu (Alibaba, Senior Manager) Wei Wei (Alibaba, Senior Engineer) *SJCC Room 211B (Concourse Level)*
	How GPU-Enabled Face Recognition Will Enhance the Consumer Experience Roger Angarita (FaceFirst, Inc., VP Product) Peter Trepp (FaceFirst, Inc., CEO) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	User Experience is Key to VDI Success, Color Accuracy is Key to User Experience Nachiket Karmarkar (NVIDIA, Technical Marketing) *Hilton Hotel Almaden 1 Room (Street Level)*	OpenACC Programming Model-User Stories, Vendor Reaction, Relevance, and Roadmap Michael Wolfe (NVIDIA, Compiler Engineer) Duncan Poole (NVIDIA, President of OpenACC) *SJCC Room 210F (Concourse Level)*
	Learning from Limited Data Tatsuya Harada (The University of Tokyo /	Visualizing ATP-Dependent Substrate-Processing Dynamics of the Human 26S	Next-Generation Diagnostics for Cardiovascular Disease Rima Arnaout (University of California, San Francisco,

RIKEN, Professor) *SJCC Room 220C (Concourse Level)*	Proteasome at Near-Atomic Resolution Youdong Mao (Peking University, Professor) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Assistant Professor of Medicine, Cardiology) *SJCC Room 220B (Concourse Level)*
NVIDIA IndeX - Implementing Cloud Services for Complex Scientific Data Visualization Marc Nienhaus (NVIDIA, Sr. Manager, Product Technology Lead) Tom-Michael Thamm (NVIDIA, Director Product Management) Brant Robertson (UC Santa Cruz, Associate Professor) *Hilton Hotel Market Room (Street Level)*	Edge Computing with Jetson TX2 for Monitoring Flows of Pedestrians and Vehicles Johan Barthelemy (SMART Infrastructure Facility - University of Wollongong, Research Fellow) Nicolas Verstaevel (SMART Infrastructure Facility - University of Wollongong, Associate Research Fellow) *SJCC Room 210A (Concourse Level)*	Production-Quality, Final-Frame Rendering on the GPU Rob Slater (Redshift, VP, Engineering) *SJCC Room 230B (Concourse Level)*
The Future OF Vehicle Intelligence and Autonomy Redefined by Le Mans 24HRS Bryn Balcombe (Roborace, Chief Strategy Officer) *SJCC Room 220A (Concourse Level)*	Optimize Deep FSMN Network Jun Yang (Alibaba, Algorithm Architect) Yongchao Liu (Ant Financial, Staff Engineer) *SJCC Room 210G (Concourse Level)*	NVSHMEM: A PGAS Library for GPU Clusters Sreeram Potluri (NVIDIA, Manager) Anshuman Goswami (NVIDIA, Senior Software Engineer) *SJCC Room 211A (Concourse Level)*
Engage GPUs to Accelerate Data Labeling for Your Training Jobs Srinivas Chitiveli (IBM, Product Manager) *SJCC Room 210D (Concourse Level)*	Using GPUs in the Development of Airborne Collision Avoidance Sheila Jaszlics (Pathfinder Systems, Inc., President) *SJCC Room 210B (Concourse Level)*	End-to-End Analysis of Large 3D Geospatial Datasets in RAPIDS John Murray (Fusion Data Science, CTO and Founder) *SJCC Room 212A (Concourse Level)*
Challenges and Opportunities of Using Machine Learning in Asset Management Gaurav Chakravorty (QPLUM LLC, CIO) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Using GPUs to Generate Reproducible Workflows to Accelerate Drug Discovery Amanda Minnich (Lawrence Livermore National Laboratory, Machine Learning Research Scientist) *SJCC Room 231 (Concourse Level)*	Connect with the Experts: Memory Management on Heterogeneous Systems Nikolay Sakharnykh (NVIDIA, Sr. Developer Technology Engineer) Lars Nyland (NVIDIA, GPU Computing Architect) Max Katz (NVIDIA, Solutions Architect) Javier Cabezas (NVIDIA, Sr. System Software Engineer) Robert Crovella (NVIDIA, Solutions Architect) Mark Hairgrove (NVIDIA, CUDA

			Driver) *SJCC Hall 3 Pod C (Concourse Level)*
	How the U.S. Congress Views AI Ned Finkle (NVIDIA, VP External Affairs) Jerry McNerney (California's 9th District, Congressman) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Understanding Buildings Using Machine Learning Javier Venegas (NOVALIAN, CTO) Constantin Chevance (NOVALIAN, Head of Product) Jean-Paul Baloche (NOVALIAN, CEO) *Hilton Hotel Almaden 2 Room (Street Level)*	Data Loading: the Next Frontier in Scale-out Deep Learning (Presented by Pure Storage) Emily Watkins (Pure Storage, emily.watkins@purestorage.com) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*
	Connect with the Experts: The Convergence of High Performance Computing and Artificial Intelligence Kamesh Arumugam (NVIDIA,) Tom Gibbs (NVIDIA,) *SJCC Hall 3 Pod D (Concourse Level)*	Connect with the Experts: Reduced Precision Inference Murat Guney (NVIDIA, Developer Technology Engineer) Chaitanya Talnikar (NVIDIA,) Hao Wu (NVIDIA, Compute Architecture) *SJCC Hall 3 Pod A (Concourse Level)*	
05:00PM - 07:00PM Special Events	Happy Hour & Exhibits *SJCC Expo Hall (Concourse Level)*		
07:00PM - 09:00PM	**Dinner with Strangers**		
07:00PM - 10:00PM Special Events	SLURM BoF: 1st class GPU support *SJCC Room 210E (Concourse Level)*	OpenACC User Group Meeting *Mosaic Restaurant*	

Wednesday, March 20, 2019

08:00AM - 10:00AM Instructor-Led Training *SJCC Lower Level*	Programming GPU-Accelerated POWER Systems with OpenACC Andreas Herten (Jülich Supercomputing Centre, Researcher GPUs in High Performance Computing) *SJCC Room LL21E (Lower Level)*	How to Boost the Performance of HPC/AI Applications Using MVAPICH2 Library Ching-Hsiang Chu (The Ohio State University, PhD Student) Ammar Ahmad Awan (The Ohio State University, Graduate Student) Dhabaleswar K (DK) Panda (The Ohio State University, Professor and University Distinguished Scholar) Hari Subramoni (The	Industrial Inspection Training (1/3): Automatic Industrial Inspection, Problem Formulation, Data Curation/Exploration/Formatting Peter Pyun (NVIDIA, Principal Solution Architect) Julius Chan (NVIDIA, Sr. Solution Architect) Andrew Liu (NVIDIA, Sr. Solution Architect) Ryan Shen (NVIDIA, Solution Architect) *SJCC Room LL20A (Lower Level)*

		Ohio State University, Research Scientist) *SJCC Room LL21D (Lower Level)*	
	Medical Image Classification Using the MedNIST Dataset Colin Compas (NVIDIA, Senior Solutions Architect) *SJCC Room LL20C (Lower Level)*	Building Autonomous Vehicles with Drive AGX Aaraadhya Narra (NVIDIA, Solutions Architect) Luke Harvey (NVIDIA, Deep Learning Institute Certified Instructor and Solutions Architect Manager) *SJCC Room LL21C (Lower Level)*	
08:00AM - 06:00PM *SJCC Street Level*	**Registration Open**		
09:00AM - 10:00AM Sessions	Latest Deep Learning Framework Container Optimizations Michael O'Connor (NVIDIA, Director of Software, Deep Learning Optimized Frameworks) Joey Conway (NVIDIA, Senior Product Manager of Deep Learning Software) *SJCC Room 212B (Concourse Level)*	Inside NVIDIA's AI Infrastructure for Self-Driving Cars Clement Farabet (NVIDIA, VP, AI Infrastructure) *SJCC Room 220A (Concourse Level)*	Meta-Optimization on a Distributed System for Deep Reinforcement Learning Iuri Frosio (NVIDIA, Senior Research Scientist) Gregory Heinrich (NVIDIA, Deep Learning Engineer) *SJCC Room 210E (Concourse Level)*
	Cloud Native ML with Kubeflow and TensorRT David Aronchick (Azure, Head of Open Source Machine Learning Strategy at Azure) *SJCC Room 210D (Concourse Level)*	Untangling Information for Training Strategies in Machine and Deep Learning Daniel Wilke (University of Pretoria, Senior Lecturer (PhD)- Department of Mechanical and Aeronautical Engin) *SJCC Room 231 (Concourse Level)*	Generative Modeling for Wireless Network Performance Optimization Bryan Larish (Verizon, Director, Technology) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*
	High-Performance Compute and Visualization Platform for Oil & Gas Companies Jay Kirby (GeoComputing Group, VP Strategic Solutions) John Creevan (GeoComputing Group, CEO) *Hilton Hotel Almaden 1 Room (Street Level)*	Advances in Computational Particle Mechanics Using GPUs Nicolin Govender (University of Surrey, Research Fellow) *Hilton Hotel Almaden 2 Room (Street Level)*	A Massively Scalable Architecture for Learning Representations from Heterogeneous Graphs C. Bayan Bruss (Capital One, Machine Learning Engineer) Anish Khazane (Capital One Research, Machine Learning Engineer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*
	AI in Diagnostic Imaging: An Opportunity to Reinvent	OpenMP 5.0 for Accelerators and What	AstroAccelerate - GPU enabled Signal Processing

the Clinical Workflow Tessa Cook (Penn Medicine, Assistant Professor of Radiology) *SJCC Room 220B (Concourse Level)*	Comes Next Bronis de Supinski (Lawrence Livermore National Laboratory, Chief Technology Officer for Livermore Computing) Tom Scogland (Lawrence Livermore National Laboratory, Computer Scientist) *SJCC Room 210G (Concourse Level)*	on the path to the Square Kilometre Array Wes Armour (Oxford eResearch Centre, Department of Engineering Science, Director) Karel Adamek (Department of Engineering Sciences, University of Oxford, Research Associate) *Hilton Hotel Market Room (Street Level)*
Deep Learning For Spatiotemporal Data Rose Yu (Northeastern University's Khoury College of Computer Sciences, Assistant Professor) *SJCC Room 210A (Concourse Level)*	Deep Learning and GPU Acceleration for VLSI Physical Design Zhiyao Xia (Duke University, Ph.D. Student) Yibo Lin (University of Texas at Austin, Postdoctoral Researcher) *SJCC Room 210B (Concourse Level)*	Prism & RTX Victor Yudin (Mill Film, Lead Software Developer) *SJCC Room 230B (Concourse Level)*
Khronos Cross-Platform Standards Update: Vulkan, SPIR-V, OpenXR, glTF, and OpenCL Neil Trevett (NVIDIA, Vice President Developer Ecosystems) *SJCC Room 230C (Concourse Level)*	Exascale Deep Learning for Climate Analytics Thorsten Kurth (Lawrence Berkeley National Laboratory, Application Performance Specialist) Josh Romero (NVIDIA, Developer Technology Engineer) *SJCC Room 211A (Concourse Level)*	Visualize Your Large Datasets Peter Messmer (NVIDIA, Sr Manager) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
Deep Learning for Robotics Pieter Abbeel (UC Berkeley | covariant.ai, Professor | Founder) *SJCC Room 220C (Concourse Level)*	Building a Distributed GPU DataFrame with Python Siu Kwan Lam (Anaconda, Software Engineer) *SJCC Room 212A (Concourse Level)*	Cloud XR: Challenges and Strategies in Streaming XR Over 5G Morgan McGuire (NVIDIA, Distinguished Research Scientist) Mohammad Khalid (Verizon, Chief Engineer XR Edge) John Benko (Orange, Principal, Emerging Wireless) James Li (Orange, Principal, AR/VR) Alisha Seam (AT&T Foundry | Palo Alto, Principal Engineer; Edge Computing Zone Lead) Tero Rissa (Nokia, Chief Architect, Machine Learning) *SJCC Room 230A (Concourse Level)*

	Smart, Active Analytical Applications: The Shift From Passive to Active Analytics (Presented by Kinetica) Irina Farooq (Kinetica, Chief Product Officer) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Architecture Considerations for Federating ML and DL Data Pipelines across Edge, Core, and Cloud (Presented by Net App) Santosh Rao (NetApp, Senior Technical Director, AI & Data Engineering) Sundar Ranganathan (NetApp, Senior Product Manager, AI) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Challenges Beyond Autonomous Driving Algorithms Jamie Carlson (NIO, VP of Autonomous Driving) *SJCC Room 210C (Concourse Level)*
10:00AM - 11:20AM Tutorials	Building and managing scalable AI infrastructure with NVIDIA DGX POD and DGX Pod Management software Darrin Johnson (NVIDIA, Global Technical Markerting for Enterprise) Andrew Bull (NVIDIA, Senior Solutions Architect) Sumit Kumar (NVIDIA, Solutions Architect) Jacci Cenci (NVIDIA, Sr. Technical Marketing Engineer) *SJCC Room 212B (Concourse Level)*		
10:00AM - 11:00AM Sessions	Data-Driven Dataset Creation: Deep Active Learning for Autonomous Vehicles and Beyond Adam Lesnikowski (NVIDIA, Senior Software Perception Engineer) *SJCC Room 220C (Concourse Level)*	Reconstruction of 3D Building Models from Aerial LiDAR with AI Dmitry Kudinov (Esri, Sr. Data Scientist) *SJCC Room 210A (Concourse Level)*	Implementing Machine Learning on GPUs Jonathan McKinney (H2O.ai, Director of Research) *SJCC Room 210E (Concourse Level)*
	TensorRT Inference with Tensorflow Guangda Lai (Google, Senior Software Engineer) Chul Gwon (Clarifai, Staff Research Scientist) Pooya Davoodi (NVIDIA, Senior Software Engineer) Trevor Morris (NVIDIA, Deep Learning Software Engineer) *SJCC Room 210D (Concourse Level)*	Metric Analysis and Performance Optimization in TensorFlow Tong Yu (Inspur Corporaton, Parallel Computing Software Engineer) *SJCC Room 231 (Concourse Level)*	Sparse Attentive Backtracking: Temporal Credit Assignment Through Reminding Nan Rosemary Ke (MILA, University of Montreal, Ph.D. Candidate) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*
	AI-Based Anomaly Detections and Threat	Designing and Implementing a VDI	Modeling Fluid Structure Interaction with Multi-GPU

Forecasting for Unified Communications Networks Tim Thornton (Ribbon Communications, Engineering Director) Kevin Riley (Ribbon Communications, CTO) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Project Powered with GPU Hardware Steve Massman (Veterans United Home Loans, Systems Engineer) *Hilton Hotel Almaden 1 Room (Street Level)*	Enabled Software Kshitiz Khanna (CertaSIM, LLC, Mechanical Engineer) *Hilton Hotel Almaden 2 Room (Street Level)*
Creation of Adversarial Accounting Records to Attack Financial Statement Audits Marco Schreyer (German Research Center for Artificial Intelligence, Researcher) Timur Sattarov (PricewaterhouseCoopers GmbH WPG, Forensic Data Analyst) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	NVIDIA Nsight Graphics: Getting The Most From Your Vulkan Applications Jeffrey Kiel (NVIDIA, Senior Manager of Graphics Tools) *SJCC Room 210F (Concourse Level)*	C++17 Parallel Algorithms for NVIDIA GPUs with PGI C++ David Olsen (NVIDIA, PGI Compiler Engineer) *SJCC Room 210G (Concourse Level)*
Deep Learning Genomics Fernanda Foertter (NVIDIA, GPU Developer Advocate (Healthcare, HPC + AI)) Johnny Israeli (NVIDIA, Manager, Deep Learning Genomics) *SJCC Room 211B (Concourse Level)*	Imaging Nearby Habitable Planets with the Largest Astronomical Telescopes and GPU-Powered Adaptive Optics Algorithms Olivier Guyon (Subaru Telescope, Associate Professor) Damien Gratadour (Observatoire de Paris & Université Paris Diderot, Associate Professor) *Hilton Hotel Market Room (Street Level)*	GPU Virtualization, 5G, and Edge Computing Make Cloud AR/VR a Reality John Benko (Orange, Principal, Emerging Wireless) James Li (Orange, Principal, AR/VR) *SJCC Room 230A (Concourse Level)*
NVIDIA Vulkan Features Update Christoph Kubisch (NVIDIA, Sr. Developer Technology Engineer) *SJCC Room 230C (Concourse Level)*	Learning-Based Predictive Models: A New Approach to Integrating Large-Scale Simulations and Experiments Brian Van Essen (Lawrence Livermore National Laboratory, Informatics Group Lead / Computer Scientist) *SJCC Room 211A (Concourse Level)*	Bringing State-of-the-Art GPU-Accelerated Molecular Modeling Tools to the Research Community John Stone (University of Illinois at Urbana-Champaign, Senior Research Programmer) *Hilton Hotel San Carlos Room (Level 2/Concourse)*
Human-Centered Interfaces for Autonomous Machines Madeline Gannon (NVIDIA, Robotics and AI R&D Engineer) *SJCC Room 210C*	Bringing Ray Tracing to Designers Ross McKegney (Adobe, Director of Engineering) *SJCC Room 230B (Concourse Level)*	Methods for Creating and Updating HD Maps for Localization and Simulation – Part 1 Graefe Gunnar (3D Mapping Solutions, Managing Director)

	(Concourse Level)		Justyna Zander (NVIDIA, Head of Mapping) Willem Strijbosch (TomTom, Head of Autonomous Driving) *SJCC Room 220A (Concourse Level)*
	Fireside Chat: Driving AI Innovation During Business Transformation Richard Karlgaard (Forbes, Publisher) Debra King (DuPont Agriculture, CIO) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*		
10:00AM - 12:00PM Sessions	Data Augmentation and GANs for Healthcare Applications Enhao Gong (Subtle Medical, CEO) Faisal Mahmood (Johns Hopkins University, Postdoctoral Fellow) *SJCC Room 220B (Concourse Level)*		
11:00AM - 12:00PM Sessions	Mixed Precision Training of Deep Neural Networks Carl Case (NVIDIA, Senior Architect) *SJCC Room 210A (Concourse Level)*	Maximizing Utilization for Data Center Inference with TensorRT Inference Server Soyoung Jeong (NVIDIA, Developer Relations Manager) David Goodwin (NVIDIA, Principal Software Engineer, Machine Learning Group) *SJCC Room 220C (Concourse Level)*	Accelerating Model Development by Reducing Operational Barriers Patrick Hayes (SigOpt, CTO) *SJCC Room 210D (Concourse Level)*
	The Frontier of Define-by-Run Deep Learning Frameworks Seiya Tokui (Preferred Networks, Inc., Researcher) *SJCC Room 210E (Concourse Level)*	Machine Learning Based Network Fault Management with Streaming Telemetry Data Vladimir Yashin (Cisco, Senior Data Scientist) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Improving Healthcare with Horizon and NVIDIA GPU Shane Limbach (Nebraska Medicine, Technical Systems Senior Engineer) *Hilton Hotel Almaden 1 Room (Street Level)*
	Ultra-Fast High-Fidelity CFD Simulations for Automotive Aerodynamics Christoph Niedermeier (Altair Engineering, Development Manager) *Hilton Hotel Almaden 2 Room (Street Level)*	TensorFlow Extended: How to Take AI from Experimentation to Production Clemens Mewald (Google Research, Product Manager) *SJCC Room 210F*	The Kokkos C++ Performance Portability EcoSystem Christian Trott (Sandia National Laboratories, Principal Member of Technical Staff) *SJCC Room 210G*

	(Concourse Level)	*(Concourse Level)*
Kipoi: Model Zoo for Genomics Ziga Avsec (Technical University Munich, PhD Student) *SJCC Room 211B (Concourse Level)*	Artificial Intelligence in Search for Extra-Terrestrial Intelligence Yunfan (Gerry) Zhang (UC Berkeley, Graduate Student Researcher in Machine Learning) *Hilton Hotel Market Room (Street Level)*	Real-Time Streaming of 3D Enterprise Applications To Low-Powered Devices Andrei Ermilov (Microsoft, Senior Software Engineer) *SJCC Room 230A (Concourse Level)*
Advancing Fusion Science With CGYRO Using GPU-Based Leadership Systems Jeff Candy (General Atomics, Manager, Turbulence and Transport Group, General Atomics) Igor Sfiligoi (General Atomics, HPC Software Developer, Energy group, General Atomics) *SJCC Room 211A (Concourse Level)*	GPU-Enhanced Collaborative Scientific Visualization Tim Biedert (NVIDIA, Senior Scientific Visualization Developer Technology Engineer) Benjamin Hernandez (Oak Ridge National Laboratory, Computer Scientist) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Simulation to Reality Transfer in Robotic Learning Stan Birchfield (NVIDIA, Principal Research Scientist) *SJCC Room 210C (Concourse Level)*
Summarize Large Text using NLP on NVIDIA P3 Instances Kristof Schum (AWS, Global Segment Leader for Machine Learning) *SJCC Room 231 (Concourse Level)*	Edge AI in Smart Manufacturing: Defect Detection and Beyond Trista Chen (Inventec Corporation, Chief Scientist of Machine Learning) Wei-Chao Chen (Skywatch Inc., Co-founder) *SJCC Room 210B (Concourse Level)*	Extreme View Synthesis: Novel View Generation Under Large Camera Motion Orazio Gallo (NVIDIA, Senior Research Scientist) *SJCC Room 230C (Concourse Level)*
Minimizing Risk While Maximizing Gain: Full Feature Space Representation While Upgrading Minimal Subset of PCs Tom Drabas (Microsoft, Developer) *SJCC Room 212A (Concourse Level)*	Applying AI to Customer Service Jared Ritter (Charter, Director of Wireless Engineering) Saurabh Kumar (Actionable Science, CEO and Co-Founder) Rob Enderle (Enderle Group, President and Principal Analyst) Satish Mandalika (Drishyam AI, CEO and Co-Founder) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Methods for Creating and Updating HD Maps for Localization and Simulation – Part 2 Geetank Raipuria (Navinfo Europe, Computer Vision Engineer) Justyna Zander (NVIDIA, Head of Mapping) Vladimir Shestak (HERE Techologies, Lead Software Engineer – Automated Driving) *SJCC Room 220A (Concourse Level)*
Shaping the Future of Design with Human + Machine Collaboration (Presented by Autodesk) Jack Dahlgren (NVIDIA,		

	Architect and Construction Manager) Brian Pene (Autodesk, Director, Autodesk Research) Joseph Joseph - Assoc. AIA (Gensler, Global Director of Design Technology) *SJCC Room 212B (Concourse Level)*		
12:00PM - 02:00PM *SJCC South Hall*	**Lunch**		
12:00PM - 07:00PM *SJCC Expo Hall 1-3*	**Exhibits**		
01:00PM - 03:00PM Instructor-Led Training *SJCC Lower Level*	NASA GeneLab Repository Deep-learning Based Accelerated Workflow and Data Curation Wamsi Viswanath (OmniSci, Data Scientist) Jacci Cenci (NVIDIA, Sr. Technical Marketing Engineer) *SJCC Room LL21E (Lower Level)*	Industrial Inspection Training (3/3) - Inferencing, Perf Assessment, Interpretation Peter Pyun (NVIDIA, Principal Solution Architect) Ryan Shen (NVIDIA, Solution Architect) Andrew Liu (NVIDIA, Sr. Solution Architect) Julius Chan (NVIDIA, Sr. Solution Architect) *SJCC Room LL20A (Lower Level)*	Building a Reinforcement Learning Agent in Starcraft 2 Eric Harper (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*
	Introduction to Autoencoders Paul Hendricks (NVIDIA, Solution Architect - Retail) *SJCC Room LL21A (Lower Level)*	Modeling Time Series Data with Recurrent Neural Networks in Keras Colin Compas (NVIDIA, Senior Solutions Architect) *SJCC Room LL21D (Lower Level)*	Vehicle Interfacing Using DriveWorks Sean Iqbal (NVIDIA, Software Solutions Architect) *SJCC Room LL21C (Lower Level)*
01:00PM - 02:00PM Sessions	Human-AI Collaboration: The New Art Duo Pinar Yanardag (AI Fiction, CEO) *SJCC Room 210E (Concourse Level)*	Network Accelerated GPU Workloads: How 5G and Edge Networks Boost XR Mohammad Khalid (Verizon, Chief Engineer XR Edge) *Hilton Hotel Almaden 1 Room (Street Level)*	How is RTX Technology Revolutionizing the Way CannonDesign Approaches Visualization? Ernesto Pacheco (CannonDesign, Director of Visualization) Hilda Espinal (CannonDesign, Chief Technology Officer) *Hilton Hotel Almaden 2 Room (Street Level)*
	CUDA Implementation of Modern Preconditioning Techniques for Iterative Solvers of Linear Systems Massimo Bernaschi	All You Need to Know about Programming NVIDIA's DGX-2 Lars Nyland (NVIDIA, GPU Computing Architect)	Training ImageNet in Four Minutes Yangzihao Wang (Tencent Technology (Beijing) Co., Ltd., Senior Software

(National Research Council of Italy, Director of Technology) *SJCC Room 210F (Concourse Level)*	Stephen Jones (NVIDIA, Principal Software Engineer) *SJCC Room 220C (Concourse Level)*	Engineer) Haidong Rong (Tencent Technology (Beijing) Co., Ltd., Senior Software Engineer) *SJCC Room 210D (Concourse Level)*
Surpassing State-of-the-Art VQA with Deep Learning Optimization Techniques and Limited GPU Resources Quang Tran (AIOZ Pte Ltd, Head of AI) Erman Tjiputra (AIOZ, CEO) *SJCC Room 231 (Concourse Level)*	A Machine Learning Method in Computational Materials Science Xueyuan Liu (Chinese Academy of Sciences, Computer Network Information Center, Master of Engineering) *SJCC Room 211A (Concourse Level)*	cuTENSOR: High-performance Tensor Operations in CUDA Paul Springer (NVIDIA, Senior Software Engineer) Andrew Kerr (NVIDIA, Senior Compute Architect) *SJCC Room 211B (Concourse Level)*
Advancing Astrophysics with the GPU-Native, Massively Parallel Code, Cholla Evan Schneider (Princeton University, Postdoctoral Research Fellow) *Hilton Hotel Market Room (Street Level)*	Enhancing Augmented and Mixed Reality with Remote GPU Rendering for Energy Applications Jeff Potts (Baker Hughes, a GE company, Senior Research Engineer) *SJCC Room 230A (Concourse Level)*	Development and Homologation of Automated Driving in the Virtual World Houssem Abdellatif (TUEV SUED, Global Head Autonomous Driving & ADAS) Tobias Dueser (AVL List GmbH, Department Manager) *SJCC Room 220A (Concourse Level)*
Achieving Deterministic Execution Times in CUDA Applications Aayush Rajoria (NVIDIA, System Software Engineer) Ashok Kelur (NVIDIA, Senior System Software Engineer) *SJCC Room 210G (Concourse Level)*	Acceleration of an Adaptive Cartesian Mesh CFD Solver in the Current Generation Processor Architectures Bharatkumar Sharma (NVIDIA, Senior Solution Architect) Harichand M V (Vikram Sarabhai Space Centre, Scientist) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Face Recognition: from Scientific Research to Commercial Products Winston Hsu (National Taiwan University, Professor) *SJCC Room 210C (Concourse Level)*
KVM GPU Virtual Machines: Maximizing Performance and Utilization on DGX Anish Gupta (NVIDIA, Principal Engineer) *SJCC Room 212B (Concourse Level)*	Deep Learning for Automated Defect Detection in Semiconductors Seokhyung Lee (SK Hynix, Project Leader) *SJCC Room 210B (Concourse Level)*	How BMW Visualizes and Interacts with Extreme Datasets with Near Zero Latency Caroline Persson (BMW Group, Data Scientist) Todd Mostak (OmniSci, CEO and Co-Founder) *SJCC Room 212A (Concourse Level)*
Drive Operational Efficiencies with AI	Event-Driven Human Performances Using	New Features in OptiX 6.0 David Hart (NVIDIA,

	Arun Subramaniyan (BHGE - Digital, Vice President Data Science & Analytics) Atif Kureishy (Teradata, VP Global Emerging Practices) Sam Charrington (This Week in Machine Learning and AI, Host) John Elliott (Accenture Digital, Managing Director) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	NVIDIA Technology Aruna Inversin (Digital Domain, Creative Director & VFX Supervisor) *SJCC Room 230C (Concourse Level)*	Senior Systems Engineer) Ankit Patel (NVIDIA, Product Management) *SJCC Room 230B (Concourse Level)*
	Connect With the Experts: NGC Greg Crider (NVIDIA, Senior Product Manager - NGC) Scott McMillan (NVIDIA, Solutions Architect) Zhenyi Huang (NVIDIA, Senior Enterprise Solutions Engineer) Ryan McCormick (NVIDIA,) Philip Rogers (NVIDIA, Chief Software Architect Compute Server) Adam Simpson (NVIDIA, Systems Software Engineer) Sabu Nadarajan (NVIDIA, Senior Enterprise Solutions Engineer for NGC and DGX Systems) Chintan Patel (NVIDIA, Manager) *SJCC Hall 3 Pod C (Concourse Level)*	Automatic Mixed Precision in PyTorch Michael Carilli (NVIDIA, Senior Developer Technology Engineer) *SJCC Room 210A (Concourse Level)*	Using Nsight Tools to Optimize the NAMD Molecular Dynamics Simulation Program David Hardy (University of Illinois at Urbana-Champaign, Senior Research Programmer) Robert Knight (NVIDIA, Software Engineer) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*
	Computing Strategies in the Era of 5G Soma Velayutham (NVIDIA, Head of AI and Accelerated Computing) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Hybrid Machine Learning with Kubeflow Pipelines and RAPIDS (Presented by Google Cloud) Sina Chavoshi (Google Cloud, Technical Program Manager, Machine Learning) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Connect with the Experts: Distributed Training with NCCL Sreeram Potluri (NVIDIA, Manager) Sylvain Jeaugey (NVIDIA, Senior Software Engineer) *SJCC Hall 3 Pod D (Concourse Level)*
	Connect with the Experts: Multi-GPU Programming Akshay Venkatesh (NVIDIA, Senior Software Engineer) Jiri Kraus (NVIDIA, Senior Devtech Compute)	Connect with the Experts: Jetson Embedded Platform Experts Zheng Liu (NVIDIA,) Felix Schmitt (NVIDIA,) Eric Work (NVIDIA,) Winnie Hsu (NVIDIA,)	Connect with the Experts: Deep Learning Libraries – cuDNN, cuBLAS, CUTLASS Faisal Zaghloul (NVIDIA,) Mathieu Zhang (NVIDIA,) Andrew Kerr (NVIDIA,

	SJCC Hall 3 Pod E (Concourse Level)	Dipen Patel (NVIDIA,) Eric Brower (NVIDIA, Senior Director, Linux Platform Software) Kismat Singh (NVIDIA,) Sanjiv Satoor (NVIDIA, Sr. Engineering Manager, Developer Tools) Sarah Adams (NVIDIA,) Sherif Eid (NVIDIA,) Amulya Yarlagadda (NVIDIA,) Abeed Salam (NVIDIA,) Shane Chen (NVIDIA,) Vincent Nguyen (NVIDIA, DLI Certified Instructor) Martin Doe (NVIDIA,) Patricia Li (NVIDIA,) John Hsu (NVIDIA,) Bridget Bissell (NVIDIA,) Phil Lawrence (NVIDIA,) Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) Jing Yang (NVIDIA,) Anisha Goel (NVIDIA,) Aayush Rajoria (NVIDIA, System Software Engineer) Nicolas Droux (NVIDIA,) Vincent Chung (NVIDIA,) Sean Pieper (NVIDIA,) Ashok Kelur (NVIDIA, Senior System Software Engineer) Jason Su (NVIDIA,) Stuart Yates (NVIDIA,) Prabhu Kuttiyam (NVIDIA,) *SJCC Hall 3 Pod A (Concourse Level)*	Senior Compute Architect) Piotr Majcher (NVIDIA,) Kevin Vincent (NVIDIA,) Khairul Kabir (NVIDIA,) Slawek Stepniewski (NVIDIA,) Yang Xu (NVIDIA,) Philippe Vandermersch (NVIDIA,) *SJCC Hall 3 Pod B (Concourse Level)*
01:00PM - 03:00PM Sessions	Clara SDK: Enabling Medical Imaging Experts to Bring AI to the Clinic Bibb Allen (American College of Radiology Data Science Institute, Chief Medical Officer) Mike Tilkin (American College of Radiology, Chief Information Officer) Woojin Kim (Nuance Communications, Chief Medical Information Officer (CMIO)) Raghu Vemula (Nuance Communications, V.P.		

	Diagnostics R&D) Neil Tenenholtz (MGH & BWH Center for Clinical Data Science, Director of Machine Learning) *SJCC Room 220B (Concourse Level)*		
02:00PM - 03:00PM Sessions	Large-Scale Video Audio Quality Assessment on VMware Platform with NVIDIA GPUs Hari Sivaraman (VMware, Staff Engineer) Lan Vu (VMware, Senior Member of Technical Staff) *SJCC Room 212B (Concourse Level)*	Determinism in Deep Learning Duncan Riach (NVIDIA, Senior Deep Learning Software Engineer) *SJCC Room 220C (Concourse Level)*	An Automatic Batching API for High Performance RNN Inference Murat Guney (NVIDIA, Developer Technology Engineer) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*
	Accelerated Hyperscale Compute for AI at the Edge Brandon Jones (IBM, Power Technical Specialist) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	From Passthrough to vGPU: PSA Group's Walk through Next-Generation VDI Benoit Bastien (NVIDIA, Professional Visualisation - SEMEA) Alain Gonzalez (Groupe PSA, Expert Graphic Systems & 3D Imagery) *Hilton Hotel Almaden 1 Room (Street Level)*	Deep Learning to Predict Regime Changes in Financial Markets Using Constrained Time Delay and Recurrent Neural Networks Yigal Jhirad (Cohen & Steers, Head of Quantitative and Derivatives Strategies) Blay Tarnoff (Cohen & Steers, Senior Application Developer and Database Architect) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*
	Fast Singular Value Decomposition on GPUs Lung-Sheng Chien (NVIDIA, Software Engineer) Samuel Rodriguez Bernabeu (NVIDIA, Software Engineer) *SJCC Room 210F (Concourse Level)*	Turbo-Boosting Neural Networks for Object Detection Hongyang Li (Chinese University of Hong Kong, Ph.D. Student) *SJCC Room 231 (Concourse Level)*	Accelerating Distributed Deep Learning Inference on multi-GPU with Hadoop-Spark Se-Yoon Oh (Agency for Defense Development, Big Data Team Leader) Hunmin Yang (Agency for Defense Development, Researcher) *SJCC Room 210E (Concourse Level)*
	GPU-Accelerated Large Scale Phase Field and Molecular Dynamics Simulations Jian Zhang (Computer Network Information Center, Professor) Shun Xu (Computer Network Information Center, Chinese Academy of Sciences, Associate	Deep Generative Models for Computational Drug Discovery David Koes (University of Pittsburgh, Assistant Professor) *SJCC Room 211B (Concourse Level)*	AI in Astrophysics: Applying Artificial Intelligence and Deep Learning to Astronomical Research Brant Robertson (UC Santa Cruz, Associate Professor) *Hilton Hotel Market Room (Street Level)*

Professor) *SJCC Room 211A (Concourse Level)*		
Volumetric Camera Systems and Light Fields Tobias Chen (Volumetric Camera Systems, Founder) *SJCC Room 230A (Concourse Level)*	Learning to Boost Robustness for Autonomous Driving Ben Firner (NVIDIA, Machine Learning R&D Engineer) *SJCC Room 220A (Concourse Level)*	Best Practices When Benchmarking CUDA Applications Bill Fiser (NVIDIA, Senior System Software Engineer) Sebastian Jodlowski (NVIDIA, Senior System Software Engineer) *SJCC Room 210G (Concourse Level)*
Resolving Spontaneous Nonlinear Multi-Physics Flow Localization in 3-D: Tackling Hardware Limit Ludovic Räss (Stanford University, Postdoctoral Researcher) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Sparse-to-Dense: Self-supervised Depth Completion from LiDAR and Monocular Camera Fangchang Ma (Massachusetts Institute of Technology, Ph.D. Candidate) *SJCC Room 210C (Concourse Level)*	High-Performance GPGPU Implementation of 2D Histogramming Mark Roulo (KLA-Tencor, Software Engineer) *SJCC Room 210B (Concourse Level)*
NVIDIA GPU Video Technologies: Overview, Applications and Optimization Techniques Abhijit Patait (NVIDIA, Director, Multimedia Software, NVIDIA) *SJCC Room 230C (Concourse Level)*	RAPIDS cuML: A Library for GPU Accelerated Machine Learning Corey Nolet (NVIDIA, Data Scientist and Senior Engineer) Onur Yilmaz (NVIDIA, Deep Learning Solution Architect) *SJCC Room 212A (Concourse Level)*	Taking Advantage of Mixed Precision to Accelerate Training Using PyTorch Myle Ott (Facebook, Research Engineer) Sergey Edunov (Facebook, Research Engineering Manager) *SJCC Room 210D (Concourse Level)*
What Every Industry Can Learn About AI from Retail Michael Hall (GOAT, Director of Data) Alex Sabatier (NVIDIA, Global Account Executive) Francois Chaubard (Focal Systems, CEO) Marco Mascorro (Fellow Robots, Co-Founder and CEO) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Ray Tracing in Vulkan Nuno Subtil (NVIDIA, Senior Devtech Engineer) *SJCC Room 230B (Concourse Level)*	Connect with the Experts: Fast Data Pre-Processing with NVIDIA Data Loading Library (DALI) Przemek Tredak (NVIDIA, Senior Developer Technology Engineer) Michał Zientkiewicz (NVIDIA, Senior Deep Learning Engineer) Janusz Lisiecki (NVIDIA, Deep Learning Manager) *SJCC Hall 3 Pod E (Concourse Level)*
Connect with the Experts: NVIDIA Transfer Learning Toolkits for Industry Specific Solutions Poonam Chitale (NVIDIA, Senior Product Manager) Andy Feng (NVIDIA, VP	Connect with the Experts: Building a Self-Driving Car with NVIDIA DRIVE AGX Jeffrey Hetherly (NVIDIA, Sr. Deep Learning Software Engineer) Jay Chen (NVIDIA,	Connect with the Experts: OpenACC Andreas Herten (Jülich Supercomputing Centre, Researcher GPUs in High Performance Computing) Jeff Larkin (NVIDIA, Senior

	Architect) Nick Becker (NVIDIA,) Alvin Ihsani (NVIDIA, Solutions Architect) Eddie Weill (NVIDIA, Solution Architect) Mahendra Roopa (NVIDIA, Sr. Product Manager) *SJCC Hall 3 Pod F (Concourse Level)*	Solution Architect) Kamal Kannan Balagopalan (NVIDIA,) Dennis Lui (NVIDIA, Senior Solution Architect) Aaraadhya Narra (NVIDIA, Solutions Architect) Nigel Star (NVIDIA,) Miguel Sainz (NVIDIA,) *SJCC Hall 3 Pod A (Concourse Level)*	DevTech Software Engineer) Guido Juckeland (Helmholtz-Zentrum Dresden-Rossendorf, Head of Computational Science Department) Sunita Chandrasekaran (University of Delaware, Assistant Professor) Michael Wolfe (NVIDIA, Compiler Engineer) Mathew Colgrove (NVIDIA, PGI Devtech Engineer) *SJCC Hall 3 Pod D (Concourse Level)*
	MXNet Computer Vision and Natural Language Processing Models Accelerated with NVIDIA Tensor Cores Przemek Tredak (NVIDIA, Senior Developer Technology Engineer) Cyrus Vahid (AWS, Principal Evangelist – AWS Deep Engine) *SJCC Room 210A (Concourse Level)*	Connect with the Experts: Deep Learning Training for Tensor Cores Ben Barsdell (NVIDIA,) Deyu Fu (NVIDIA,) J Jiang (NVIDIA,) Michael Carilli (NVIDIA, Senior Developer Technology Engineer) Nathan Luehr (NVIDIA, Senior Developer Technology Engineer) Carl Case (NVIDIA, Senior Architect) Sharan Chetlur (NVIDIA,) *SJCC Hall 3 Pod C (Concourse Level)*	
03:00PM - 04:20PM Tutorials	Updates on Professional VR Ingo Esser (NVIDIA, Sr. Devtech Engineer) Jan Robert Menzel (NVIDIA, Sr. Devtech Engineer) *SJCC Room 230A (Concourse Level)*		
03:00PM - 04:00PM Sessions	Pushing the Limits of AI with NVIDIA GPUs and Mellanox Interconnect Gil Bloch (Mellanox Technologies, Principal Architect) *SJCC Room 212B (Concourse Level)*	Doing More with More: Recent Achievements in Large-Scale Deep Reinforcement Learning Adam Stooke (UC Berkeley, PhD Student) *SJCC Room 220C (Concourse Level)*	Using Deep Learning to Transform Internet Scale Web Searches Adi Oltean (Microsoft, Principal Software Design Engineer) Guhan Suriyanarayanan (Microsoft, Principal Program Manager) *SJCC Room 210E (Concourse Level)*
	Secure and Efficient Image Recognition Applications	Expert Panel Discussion: See How NVIDIA Quadro	Deep Learning for NLP on Small Data Sets

on a 5G Network Toshiki Sakai (NTT DOCOMO, INC., Data Scientist) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Virtual Workstations are Transforming Industries Dane Young (YOUNG TECHNOLOGIES, LLC ("YOUNGTECH"), Independent Consultant) Jared Cowart (NVIDIA, Product Manager) Sean Massey (VMware, Senior Technical Architect) Tony Foster (Dell EMC, Principal Advisor, Technical Marketing) *Hilton Hotel Almaden 1 Room (Street Level)*	Hanoz Bhathena (UBS, Data Scientist) Raghavachari Madhavan (UBS, Chief Data Scientist, UBS Research) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*
Shaping the Future of Medical Ultrasound Imaging with Deep Learning and GPU Computing Raphael Prevost (ImFusion, Senior Scientist) *SJCC Room 220B (Concourse Level)*	Scalable K-Core Decomposition for Static Graphs Using a Dynamic Graph Data Structure Alok Tripathy (Georgia Institute of Technology, Student) *SJCC Room 210F (Concourse Level)*	Deep Learning with Myia Olivier Breuleux (MILA, Computer Analyst) *SJCC Room 210D (Concourse Level)*
Higher Performance with Less Data Via Capsule Networks and Active Learning Chris Aasted (Lockheed Martin, Staff Software Engineer) *SJCC Room 231 (Concourse Level)*	Robust Power Estimation and Simultaneous Switching Noise-Prediction Methods Using Machine Learning Yan Zhang (NVIDIA, Senior Research Scientist) Seyed Nima Mozaffari (NVIDIA, Senior Hardware Engineer) Bonita Bhaskaran (NVIDIA, Senior Hardware Engineer) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Molecular Generative VAEs: Parallelization, Optimization, and Latent Space Analysis on the DGX-1 Ellen Du (The Dow Chemical Company, Research Scientist) Joey Storer (The Dow Chemical Company, Principal Research Scientist) *SJCC Room 211B (Concourse Level)*
VTK-m: Lessons from Building a Visualization Toolkit for Massively Threaded Architectures Robert Maynard (Kitware, Inc, Principal Engineer) *Hilton Hotel Market Room (Street Level)*	PLDC on NuFlare MBM-1000: 540 TB of Inline MPC in 10 Hours Harold Zable (D2S, Inc., Chief Technical Wizard) *SJCC Room 210B (Concourse Level)*	Interactive High-Fidelity Biomolecular and Cellular Visualization with RTX Ray Tracing APIs John Stone (University of Illinois at Urbana-Champaign, Senior Research Programmer) *SJCC Room 230B (Concourse Level)*
Accelerating Product Design with NVIDIA AI Denoiser Jeremy Wilkens (ID Group, Industrial Designer / Principal) Brian Hillner (Dassault	Multimodal Affects Analysis: The Future of the Autonomous Vehicle In-Cabin Experience Taniya Mishra (Affectiva, Director of AI Research and Lead Speech	Using CUDA on Windows Raphael Boissel (NVIDIA, Sr. System Software Engineer) *SJCC Room 210G (Concourse Level)*

Systemes SOLIDWORKS, Senior Product Portfolio Manager) *Hilton Hotel Almaden 2 Room (Street Level)*	Scientist) Mohammad Mavadati (Affectiva, Lead Computer Vision Scientist) *SJCC Room 220A (Concourse Level)*	
Performance of a Compressible DNS Code on the Latest GPU Architectures Maruthi Naliganahalli Hanumantharayappa (Jawaharlal Nehru Centre for Advanced Scientific Research, Research Associate) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	GPU-Accelerated 3D Point Cloud Processing with Hierarchical Gaussian Mixtures Benjamin Eckart (NVIDIA, Post-Doctoral Researcher) *SJCC Room 210C (Concourse Level)*	GstCUDA: Easy GStreamer and CUDA Integration Michael Grüner (RidgeRun, Technical Lead) Daniel Garbanzo Hidalgo (RidgeRun, Embedded Software Developer) *SJCC Room 230C (Concourse Level)*
BlazingSQL on RAPIDS: SQL for Apache Arrow in GPU Memory. Connect Data Lakes to RAPIDS William Malpica (BlazingDB, VP of Engineering) Rodrigo Aramburu (BlazingDB, CEO) Felipe Aramburu (BlazingDB, CTO) *SJCC Room 212A (Concourse Level)*	Fireside Chat - Healthcare in the AI Era: Innovating with Data and Its Implications Carla Leibowitz (NVIDIA, Global Head, Clinical and Life Sciences Partnerships) Richard White (OSU Wexner Medical Center, Chair of Radiology) Rajeev Ronanki (Anthem Insurance, Chief Digital Officer) Walter De Brouwer (Doc.AI, CEO) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Connect with the Experts: NVIDIA Video Technologies - Video, Capture and Optical Flow SDK Thomas True (NVIDIA, Senior Applied Engineer, Professional Video and Image Processing) Abhijit Patait (NVIDIA, Director, Multimedia Software, NVIDIA) *SJCC Hall 3 Pod A (Concourse Level)*
Connect with the Experts: CUDA Fortran for NVIDIA GPUs Brent Leback (NVIDIA, PGI Customer and Premier Services Manager) *SJCC Hall 3 Pod B (Concourse Level)*	Connect with the Experts: Vulkan and OpenGL Christoph Kubisch (NVIDIA, Sr. Developer Technology Engineer) Neil Trevett (NVIDIA, Vice President Developer Ecosystems) Nuno Subtil (NVIDIA, Senior Devtech Engineer) *SJCC Hall 3 Pod C (Concourse Level)*	OmniSci and RAPIDS: End-to-End Open Source Data Science Workflow (Presented by OmniSci) Venkat Krishnamurthy (OmniSci, VP of Product Management) Aaron Williams (OmniSci, VP of Global Community) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*
Connect with the Experts: CUDA & Graphics Developer Tools Sebastien Domine (NVIDIA,) Robert Knight (NVIDIA, Software Engineer) Dmitry Polyanitsa (NVIDIA,	Health Data Science at Scale John Gounley (ORNL, Computational Scientist) *SJCC Room 211A (Concourse Level)*	

	) Rafael Campana (NVIDIA, Sr. Engineering Manager, Developer Tools) Steve Ulrich (NVIDIA, Manager: Compute Debugger) *SJCC Hall 3 Pod E (Concourse Level)*		
03:00PM - 05:00PM Instructor-Led Training	Deep Autoencoders for Recommendation Systems Paul Hendricks (NVIDIA, Solution Architect - Retail) *SJCC Room LL21A (Lower Level)*	Image Classification with TensorFlow: Radiomics - 1p19q Chromosome Status Classification David Nola (NVIDIA, Deep Learning Solutions Architect) *SJCC Room LL21D (Lower Level)*	CUDA on DRIVE AGX Le An (NVIDIA, Senior Deep Learning Software Engineer) *SJCC Room LL21C (Lower Level)*
	Image Super Resolution using Autoencoders Gunter Roeth (NVIDIA, Solutions Architect) *SJCC Room LL20A (Lower Level)*		
04:00PM - 05:00PM Sessions	vMotion for NVIDIA GRID vGPU Virtual Machines: Case Study of vMotion Using MLaaS Dimitrios Skarlatos (University of Illinois at Urbana-Champaign, PhD Student in Computer Science) Hari Sivaraman (VMware, Staff Engineer) *SJCC Room 212B (Concourse Level)*	Revolutionary Voice Enhancement in Real-Time Communications with GPU Arto Minasyan (2Hz, Inc, CTO) Davit Baghdasaryan (2Hz, Inc, CEO) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	How GPU-Accelerated Virtual Workstations Enable Mobility and Collaboration for Autodesk Applications Jimmy Rotella (NVIDIA, Sr. Solutions Architect) Andrew Schilling (CannonDesign, Chief Infrastructure Officer) Jeremy Stroebel (Browning Day Mullins Dierdorf, IT Director) *Hilton Hotel Almaden 1 Room (Street Level)*
	GPU-Accelerated AI Applications for Smart Civil Infrastructure Zheng Wu (Bentley Systems, Incorporated, Ph.D., Bentley Fellow) *Hilton Hotel Almaden 2 Room (Street Level)*	Factor Investing Using Deep Learning Steven Thornton (RN Financial Corporation, Data Scientist) Rafael Nicolas Fermin Cota (RN Financial Corporation, CEO) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Ultrasound Architecture and Platform for Real-Time Image Processing Richard Tobias (Cephasonics Ultrasound Solutions, CEO) *SJCC Room 220B (Concourse Level)*
	Tensor Core Performance and Precision Josef Schule (Technische Universität Kaiserslautern, Lecturer) *SJCC Room 210F*	Using Tensor Swapping and NVLink to Overcome GPU Memory Limits with TensorFlow Samuel Matzek (IBM, Senior Software Engineer)	Automatic Model Tuning in Practice Using Bayesian Hyperparameter Tuning Cyrus Vahid (Amazon Web Services, Principal Evangelist - AWS AI Labs)

(Concourse Level)	*SJCC Room 210E (Concourse Level)*	*SJCC Room 210D (Concourse Level)*
MATLAB and NVIDIA Docker: A Complete AI Solution, Where You Need It, in an Instant Jos Martin (MathWorks, Engineering Manager) Joss Knight (MathWorks, Developer) *SJCC Room 231 (Concourse Level)*	Real-Time Connection-Based Filtering to Improve the Precision of the Search Engine in Life Sciences Vatsal Agarwal (Innoplexus Consulting Services Pvt. Ltd., Vice President - Technology & Innovation) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Petascale Molecular Dynamics Simulations on the Summit POWER9/Volta Supercomputer James Phillips (University of Illinois, Senior Research Programmer) *SJCC Room 211A (Concourse Level)*
How to Scale from Workstation through Cloud to HPC in Cryo-EM Processing Lance Wilson (Monash University, Senior HPC Consultant) *SJCC Room 211B (Concourse Level)*	Transforming Matter at Extreme Conditions: Crystallization and Self-Assembly for New Materials Edward W. Lowe, Jr. (Will) (FitNow, Inc, Senior Data Scientist / Director of Lose It! Labs) Jonathan Belof (Lawrence Livermore National Laboratory, Group Leader and Physicist) *Hilton Hotel Market Room (Street Level)*	Deep Learning for Autonomous Driving at BMW Alexander Frickenstein (BMW Group, PhD Candidate) *SJCC Room 220A (Concourse Level)*
Optimizing Large Reductions in BerkeleyGW with CUDA, OpenACC, OpenMP 4.5 and Kokkos Charlene Yang (Lawrence Berkeley National Laboratory, Application Performance Consultant) Rahulkumar Gayatri (Lawrence Berkeley National Laboratory, PostDoc) *SJCC Room 210G (Concourse Level)*	Unstructured Grid CFD Algorithms for NVIDIA GPUs Aaron Walden (NASA Langley Research Center, Computer Scientist) Eric Nielsen (NASA Langley Research Center, Research Scientist) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Deep Neural Network Pruning for Efficient Edge Computing in IoT Mohammad Jahanshahi (Purdue University, Assistant Professor) Rih-Teng Wu (Purdue University, Research Assistant) *SJCC Room 210C (Concourse Level)*
Synthetic Data will Drive Next Wave of Business Applications Rev Lebaredian (NVIDIA, Vice President, Simulation Technology) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Using Machine Learning for VLSI Testability and Reliability Mark Ren (NVIDIA, Principal Research Scientist) Miloni Mehta (NVIDIA, Senior VLSI Clocks Engineer) *SJCC Room 210B (Concourse Level)*	Employing Deep Learning for Automatic Analysis of Conventional and 360° Video Hannes Fassold (JOANNEUM RESEARCH, Senior Researcher) *SJCC Room 230C (Concourse Level)*
Scale Your Studio: GPU Rendering on the Cloud Chris Bond (Amazon Web	Effective, Scalable Multi-GPU Joins Jiri Kraus (NVIDIA, Senior	Connect with the Experts: Deep Learning Developer Tools for Network

	Services, Founder, Thinkbox) *SJCC Room 230B (Concourse Level)*	Devtech Compute) Tim Kaldwey (NVIDIA, Manager DevTech AI) Nikolay Sakharnykh (NVIDIA, Sr. Developer Technology Engineer) *SJCC Room 212A (Concourse Level)*	Optimization Poonam Chitale (NVIDIA, Senior Product Manager) David Zier (NVIDIA, SW Engineering Manager) Holly Wilper (NVIDIA, Manager in Systems Software Engineering) Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) Sanjiv Satoor (NVIDIA, Sr. Engineering Manager, Developer Tools) Gaoyan Xie (NVIDIA, Senior Software Engineering Manager of DL Developer Tools) *SJCC Hall 3 Pod A (Concourse Level)*
	Training AI Models Faster With Distributed Training in PyTorch Teng Li (Facebook, Research Scientist, Facebook AI Research) Soumith Chintala (Facebook, Software Engineering Manager, Facebook AI Research) *SJCC Room 220C (Concourse Level)*	GDDR Memory Enabling AI and High-Performance Compute (Presented by Micron) Wolfgang Spirkl (Micron Technology, Inc, Fellow) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	NVIDIA Quadro: The Fusion of Graphics and AI (Presented by PNY Technologies) Carl Flygare (PNY Technologies, Quadro Product Marketing Manager) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*
	AresDB: A GPU-Powered Real-Time Analytical Engine Jeremy Shi (UBER, Senior Software Engineer) Kate Zhang (UBER, Senior Engineering Manager) Jian Shen (UBER, Senior Software Engineer) *SJCC Room 210A (Concourse Level)*	Connect with the Experts: Performance Analysis and Optimization Kamesh Arumugam (NVIDIA,) Moises Hernandez (NVIDIA, -) Peng Wang (NVIDIA,) *SJCC Hall 3 Pod F (Concourse Level)*	Connect with the Experts: Jetson Embedded Platform Experts Zheng Liu (NVIDIA,) Felix Schmitt (NVIDIA,) Eric Work (NVIDIA,) Winnie Hsu (NVIDIA,) Dipen Patel (NVIDIA,) Eric Brower (NVIDIA, Senior Director, Linux Platform Software) Kismat Singh (NVIDIA,) Sanjiv Satoor (NVIDIA, Sr. Engineering Manager, Developer Tools) Sarah Adams (NVIDIA,) Sherif Eid (NVIDIA,) Amulya Yarlagadda (NVIDIA,) Abeed Salam (NVIDIA,) Shane Chen (NVIDIA,) Vincent Nguyen (NVIDIA, DLI Certified Instructor) Martin Doe (NVIDIA,) Patricia Li (NVIDIA,) John Hsu (NVIDIA,)

			Bridget Bissell (NVIDIA,) Phil Lawrence (NVIDIA,) Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) Jing Yang (NVIDIA,) Anisha Goel (NVIDIA,) Aayush Rajoria (NVIDIA, System Software Engineer) Nicolas Droux (NVIDIA,) Vincent Chung (NVIDIA,) Sean Pieper (NVIDIA,) Ashok Kelur (NVIDIA, Senior System Software Engineer) Jason Su (NVIDIA,) Stuart Yates (NVIDIA,) Prabhu Kuttiyam (NVIDIA,) *SJCC Hall 3 Pod B (Concourse Level)*
	Vive Pro Eye Tracking and Foveated Rendering with VRS (Presented by HTC Vive) Cory Corvus (HTC Vive, Developer Relations Engineer) *SJCC Room 230A (Concourse Level)*	Connect with the Experts: Deep Learning Libraries – cuDNN, cuBLAS, CUTLASS Faisal Zaghloul (NVIDIA,) Mathieu Zhang (NVIDIA,) Andrew Kerr (NVIDIA, Senior Compute Architect) Piotr Majcher (NVIDIA,) Kevin Vincent (NVIDIA,) Khairul Kabir (NVIDIA,) Slawek Stepniewski (NVIDIA,) Yang Xu (NVIDIA,) Philippe Vandermersch (NVIDIA,) *SJCC Hall 3 Pod C (Concourse Level)*	
05:00PM - 07:00PM Special Events	Happy Hour & Exhibits *SJCC Expo Hall (Concourse Level)*		
07:00PM - 10:00PM Special Events	NGC User Meetup *SJCC Room 210E (Concourse Level)*		
07:30PM - 09:30PM	**Dinner with Strangers**		

Thursday, March 21, 2019

08:00AM - 04:00PM *SJCC Street Level*	**Registration Open**		
08:00AM - 10:00AM Instructor-Led Training *SJCC*	Learning Graph Neural Networks with Deep Graph Library	Intro to Image Captioning Laura Montoya (accel.ai, Founder)	Training Semantic Segmentation for NVIDIA DRIVE

Lower Level	Lingfan Yu (New York University, Ph.D. Student) Minjie Wang (New York University, Ph.D. Student) *SJCC Room LL21E (Lower Level)*	*SJCC Room LL21D (Lower Level)*	Jeffrey Hetherly (NVIDIA, Sr. Deep Learning Software Engineer) *SJCC Room LL21C (Lower Level)*
	Predicting Remaining Useful Life (1/2) Steven White (NVIDIA, Deep Learning Solution Architect) *SJCC Room LL20A (Lower Level)*	AI-Assisted Annotation for Medical Imaging Alvin Ihsani (NVIDIA, Solutions Architect) *SJCC Room LL20C (Lower Level)*	
09:00AM - 10:00AM Sessions	Dynamic Sharing of GPUs and IO in a PCIe Network Haakon Stensland (Simula Research Laboratory, Research Scientist) *SJCC Room 212B (Concourse Level)*	TensorCore Optimized DNN for Efficient Low Latency Inference for 5G Networks Tero Rissa (Nokia, Chief Architect, Machine Learning) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Deliver Extreme Graphics While Achieving Great TCO with HPE Simplivity HCI & NVIDIA GPUs Thomas Poppelgaard (Independent, Technology Evangelist/Consultant) Prashanto Kochavara (HPE, HPE Simplivity Product Management) *Hilton Hotel Almaden 1 Room (Street Level)*
	AI/Deep Learning: Transformational Health Care Use Cases Dima Rekesh (Optum Tech - UHG, Senior Distinguished Engineer) Julie Zhu (Optum Technology, United Health Group, Distinguished Engineer/Chief Data Scientist) *SJCC Room 220B (Concourse Level)*	Tuning the Un-Tunable: Lessons for Tuning Expensive Deep Learning Functions Scott Clark (SigOpt, CEO) *SJCC Room 210A (Concourse Level)*	Reaching Beyond Human Accuracy with AI Datacenters Greg Diamos (Baidu, Research Lead) *SJCC Room 210D (Concourse Level)*
	Neural Networks Designing New Drugs: The Rise of the Machines Mariya Popova (University of North Carolina at Chapel Hill, Graduate Student) *SJCC Room 231 (Concourse Level)*	Commercial AI Framework with JD.com's NVIDIA's GPU-Powered Advertisement Business Juiyang Chang (JD.com, Machine Learning Engineer) Xianghong Luo (JD.com, Senior Staff Scientist) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	The Rocky Road to Tasking Laura Morgenstern (Jülich Supercomputing Centre, PhD Student Computer Science) Ivo Kabadshow (Jülich Supercomputing Centre, Scientist) *SJCC Room 211A (Concourse Level)*
	GPUs for Data Acquisition and Simulation for the Muon g-2 Experiment at Fermilab Ran Hong (Argonne National Lab, Postdoctoral	AI Manufacturing Innovation Joseph Wang (Foxconn Interconnect Technology (FIT), CTO) Allen Lee (Foxconn	Hyundai Breaks the Boundaries: GPU-Powered Integration of Active Noise and Sound Control for Future Mobility Kang-duck Ih (NVH

Research Associate) Wesley Gohn (Siemens Healthineers, Postdoc Research Associate) *Hilton Hotel Market Room (Street Level)*	Interconnect Technology (FIT), Manager, FIT Engineering Analysis) *SJCC Room 210B (Concourse Level)*	Research Lab, Hyundai Motor Company, Research Fellow) *SJCC Room 220A (Concourse Level)*
Synchronization is Bad, but if You Must ... Olivier Giroux (NVIDIA, Distinguished Architect) *SJCC Room 210F (Concourse Level)*	MagLev: A Production-grade AI Platform Running on GPU-enabled Kubernetes Clusters Divya Vavili (NVIDIA, Senior Software Engineer, AI Infrastructure) Yaya Khoja (NVIDIA, Technical Product Manager, AI Infrastructure) *SJCC Room 210E (Concourse Level)*	Simplifying AI for Communications, Radar, and Wireless Systems John Ferguson (Deepwave Digital, Inc, CEO) *SJCC Room 212A (Concourse Level)*
Graduate Fellowship FastForward Talks Aishwarya Agrawal (Georgia Tech, PhD Candidate) Abhishek Badki (UC Santa Barbara, PhD Candidate) Bill Dally (NVIDIA, Chief Scientist) Philippe Tillet (Harvard University, PhD Candidate) Huizi Mao (Stanford University, PhD Candidate) Daniel George (University of Illinois at Urbana-Champaign, PhD Candidate) Xun Huang (Cornell University, PhD Candidate) William Yuan (Harvard University, PhD Candidate) Zhilin Yang (Carnegie Mellon University, PhD Candidate) Ana Serrano (Universidad de Zaragoza, PhD Candidate) *SJCC Room 220C (Concourse Level)*	PhysX 4: Raising the Fidelity and Performance of Physics Simulation in Games Michelle Lu (NVIDIA, Principal Software Engineer) Kier Storey (NVIDIA, Principal Software Engineer) Gordon Yeoman (NVIDIA, Software Engineer) *SJCC Room 230B (Concourse Level)*	Deep Learning At The Edge with AWS SageMaker and NVIDIA Jetson Platforms Samir Araujo (Amazon Web Services, Solutions Architect) Luciano Martins (Amazon Web Services, Solutions Architect) *SJCC Room 210C (Concourse Level)*
Mapping Informal Settlements in Developing Countries Using Machine Learning Patrick Helber (German Research Center for	Deep Learning Hardware Architectures with Deep Thinking (Presented by QCT) James Jau (QCT, VP of Research and	HP Global Go-to-Market OEM Offering (Presented by HP) Jorge Lopez (HP, Inc., Global Z by HP OEM Business Manager)

	Artificial Intelligence (DFKI), PhD Researcher) Benjamin Bischke (German Research Center for Artificial Intelligence (DFKI), PhD Researcher) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Development) Michael Quan (QCT, Director Presales Engineering) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Jacci Cenci (NVIDIA, Sr. Technical Marketing Engineer) *SJCC Room 210G (Concourse Level)*
	How Pixvana uses NVIDIA VR Works SDK in the Cloud to Stitch 8k+ 360-degree Videos and Turn them into Interactive VR Videos for Enterprise Training Scott Squires (Pixvana, CTO/CD) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	2 Pivotal Memory Technologies Enabling New Generation of AI Workloads (Presented by Samsung) Tien Shiah (Samsung Semiconductor, Inc., Sr. Manager, Memory Marketing) *SJCC Room 210G (Concourse Level)*	Moving VR Upstream in Workflows Jason Welsh (Accenture, Managing Director) Aakash Indurkhya (Virtualitics, Head of Machine Learning Projects) Daniel Thomas (Gravity Sketch, CTO) Jonathan Gagne (MasterpieceVR, CEO) Dave Tyner (Autodesk, Inc., Thought Leadership Program Manager, Autodesk Construction) *SJCC Room 230A (Concourse Level)*
	Reconnaissance Blind Chess (RBC): A Challenge Problem for Planning and Autonomy Casey Richardson (Johns Hopkins University Applied Physics Lab, Group Chief Scientist) *Hilton Hotel San Carlos Room (Level 2/Concourse)*	Better Vision for Computer Vision Nathan Wheeler (Entropix, CEO) *SJCC Room 211B (Concourse Level)*	
10:00AM - 11:00AM Sessions	GPU Resource Pooling and the benefit of deploying CUPTI Lingjie Xu (Alibaba, Director of Applied AI Infrastructure) Junrui Zhou (Alibaba Inc, Senior Engineer) Lingling Jin (Alibaba Inc, Senior Manager) *SJCC Room 212B (Concourse Level)*	Softwareization of Mobile Radio Networks Alexander Keller (NVIDIA, Director of Research) Slawomir Stanczak (TU Berlin, Professor) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*	Using Industry Standard Benchmark Tools To Size Graphics Accelerated Applications Michael Brennan (Cisco, Product Manager, Virtual Client Computing and Graphics) *Hilton Hotel Almaden 1 Room (Street Level)*
	Machine Learning for Automated Context Discovery of Commercial Buildings Kushagra Thakur (Honeywell, AI Technology Lead) *SJCC Room 230B*	Machine Reasoning: A Perspective and Possibility Zhangsheng Lai (NVIDIA, Solutions Architect) Aik Beng Ng (NVIDIA, Senior Solutions Architect) *SJCC Room 210F (Concourse Level)*	How To Use GPUs For Faster, Better and Cheaper Drug Development Gaurav Tripathi (Innoplexus AG, CTO) *SJCC Room 220B (Concourse Level)*

(Concourse Level)		
Optimizing Runtime Performance of Neural Net Architectures for High Scalability John Kominek (Voci Technologies, Chief Technology Officer) *SJCC Room 210A (Concourse Level)*	Video-to-Video Synthesis Ting-Chun Wang (NVIDIA, Research Scientist) *SJCC Room 210E (Concourse Level)*	Faster Neural Nets with Hardware-Aware Architecture Learning Elad Eban (Google, Research Scientist) *SJCC Room 210D (Concourse Level)*
How AI is Changing the Way to Understand the Earth and Us? Taegyun Jeon (SI Analytics, Founder and CEO) *SJCC Room 231 (Concourse Level)*	Training Spiking Neural Networks on GPUs with Bidirectional Interleaved Complementary Hierarchical Networks Anusha Swamy (ORBAI, AI Engineer) Brent Oster (ORBAI, CEO) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	PGI Compilers, The NVIDIA HPC SDK: Updates for 2019 Michael Wolfe (NVIDIA, Compiler Engineer) *SJCC Room 211A (Concourse Level)*
CUDA-Accelerated Short-Read Alignment to a Large Reference Genome Richard Wilton (Johns Hopkins University, Associate Research Scientist) *SJCC Room 211B (Concourse Level)*	Lattice QCD with Tensor Cores Jiqun Tu (Columbia University, Graduate Student) *Hilton Hotel Market Room (Street Level)*	Deep Learning at BMW Logistics Norman Müller (BMW Group, Data Scientist) Dylan Sheppard (BMW Group, Product Owner - Digital Design & Virtual Reality) *SJCC Room 210B (Concourse Level)*
Democratizing 3D Modeling using Virtual Reality and Machine Learning Jonathan Gagne (MasterpieceVR, CEO) *SJCC Room 230A (Concourse Level)*	Real-Time Ray Tracing with MDL Materials Ignacio Llamas (NVIDIA, Director, Distinguished Engineer, Real Time Ray Tracing) Max Aizenshtein (NVIDIA, Senior System Software Engineer) *SJCC Room 230C (Concourse Level)*	Helping Autonomous Vehicles See the World Angus Pacala (Ouster, CEO) Tsuyoshi Hara (SONY Electronics Inc., Automotive Solution Architect) *SJCC Room 220A (Concourse Level)*
Enabling Level 4 Autonomous Driving Technologies Jianxiong Xiao (AutoX Technologies, Inc., Founder & CEO) *SJCC Room 210C (Concourse Level)*	Context-Aware Network Mapping and Asset Classification Bartley Richardson (NVIDIA, Senior Data Scientist (AI Infrastructure)) *SJCC Room 212A (Concourse Level)*	AI for Human Rights Investigations Julien Cornesbise (Element AI, Director of Research, AI for Good, Head of London Office) Josh Lyons (Human Rights Watch, Director of Geospatial Analysis) Fred Abrahams (Human Rights Watch, Associate Program Director) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*

	Accelerating Your AI Journey (Presented by Lenovo) Madhu Matta (Lenovo, VP & GM, High Performance Computing & Artificial Intelligence) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*	Z by HP Data Science Workstation: Customer Use Cases (Presented by HP) Bruce Blaho (HP, Inc., HP Fellow & VP, Z by HP Chief Technologist) Jacci Cenci (NVIDIA, Sr. Technical Marketing Engineer) *SJCC Room 210G (Concourse Level)*	Text-to-Speech: Overview of the Latest Research using Tacotron2 and Waveglow with Tensor Core performance Rafael Valle (NVIDIA, Research Scientist) Yang Zhang (NVIDIA, Deep Learning Engineer) Ryan Prenger (NVIDIA, Senior Deep Learning Researcher) *SJCC Room 220C (Concourse Level)*
10:00AM - 12:00PM Instructor-Led Training *SJCC Lower Level*	Deep Learning for Image Classification and Time-Series Forecasting in MATLAB Pitambar Dayal (MathWorks, Partner Manager) *SJCC Room LL21E (Lower Level)*	Deployment of Semantic Segmentation Network Using TensorRT Houman Abbasian (NVIDIA, Senior Deep Learning Software Engineer) *SJCC Room LL21C (Lower Level)*	Image Creation using Generative Adversarial Networks with TensorFlow and DIGITS Gary Burnett (NVIDIA, Solution Architect) *SJCC Room LL21A (Lower Level)*
	Data Science Workflows for Deep Learning in Medical Applications David Nola (NVIDIA, Deep Learning Solutions Architect) *SJCC Room LL21D (Lower Level)*	Predicting Remaining Useful Life (2/2) *SJCC Room LL20A (Lower Level)*	
11:00AM - 02:00PM *SJCC Expo Hall 1-3*	**Exhibits**		
11:00AM - 12:00PM Sessions	Unprivileged GPU Containers on a LXD Cluster Christian Brauner (Canonical Ltd., Kernel Engineer) Stéphane Graber (Canonical Ltd., Engineering Manager) *SJCC Room 212B (Concourse Level)*	Distributed Deep Learning Mathew Salvaris (Microsoft, Senior Data Scientist) *SJCC Room 210D (Concourse Level)*	Machine Learning for Radio Communications with TensorRT and Xavier Tim O'Shea (DeepSig, CTO) *Marriott Hotel Ballroom 5 (Level 2/Concourse)*
	Leveraging NVIDIA, Citrix and IGEL to Support Unified Communications in VDI Simon Clephan (IGEL, Public Relations) Jeff Kalberg (IGEL Technology, Technology Evangelist) *Hilton Hotel Almaden 1*	A Deep Learning-Based Method for Automated Volumetric Assessment of Liver Lesions Daniel Golden (Arterys, Director of Machine Learning) *SJCC Room 220B (Concourse Level)*	Infusing Physics into Deep Learning Algorithms with Applications to Stable Landing of Drones Anima Anandkumar (NVIDIA, Director of ML Research) *SJCC Room 220C (Concourse Level)*

Room (Street Level)		
Up and Running with Kubeflow Anywhere Tim Van Steenburgh (Canonical, Engineering Manager) *SJCC Room 210A (Concourse Level)*	Data2Vis: Automatic Generation of Data Visualizations Using Sequence-to-Sequence Recurrent Neural Networks. Victor Dibia (Cloudera Fast Forward Labs, Research Engineer) *SJCC Room 210E (Concourse Level)*	Cloud-to-Edge AI: Micro-Climate Anomaly Detection and Plant Stress Monitoring in the Amazon Biosphere Miro Enev (NVIDIA, Sr. Solution Architect) Wenming Ye (AWS, Specialist Solution Architect) *SJCC Room 231 (Concourse Level)*
Multi-GPU FFT Performance on Different Hardware Configurations Kevin Roe (Maui High Performance Computing Center, High Performance Computing SME) *SJCC Room 211A (Concourse Level)*	Real-Time Vehicle Inspection with AI-Aided Computer Vision Matthew Cook (BMW Group, Data Scientist) Marcin Ziolkowski (BMW Group, Data Scientist) *SJCC Room 210B (Concourse Level)*	Advances in Real-Time Automotive Visualisation Chris O'Connor (ZeroLight, Technical Director) *SJCC Room 230A (Concourse Level)*
Deep Sensor Fusion for Visible Stereo and Thermal Stereo for Autonomous Driving Vijay John (Toyota Technological Institute, Assistant Professor) Yuquan Xu (Toyota Technological Institute, Researcher) *SJCC Room 220A (Concourse Level)*	Strong Scaling HPC Applications: Best Practices with a Lattice QCD case study Kate Clark (NVIDIA, Principal Developer Technology Engineer) Mathias Wagner (NVIDIA, Senior Developer Technology Engineer) *Hilton Hotel Market Room (Street Level)*	UCX-Python: A Flexible Communication Library for Python Applications Akshay Venkatesh (NVIDIA, Senior Software Engineer) *SJCC Room 210F (Concourse Level)*
Scaling Digital Crime Investigations with Massive Visual Graphic Analytics Leo Meyerovich (Graphistry, CEO) *SJCC Room 212A (Concourse Level)*	Exploring Ray-Traced Future in Metro Exodus Sergei Karmalsky (4A-Games, Art Director) Ben Archard (4A-Games, Programmer) Dmitry Zhdan (NVIDIA, DevTech Engineer) Oles Shyshkovtsov (4A-Games, CTO) *SJCC Room 230B (Concourse Level)*	Connect with the Experts: TensorRT Boris Fomitchev (NVIDIA,) Dilip Sequeira (NVIDIA,) Micah Villmow (NVIDIA, Senior Deep Learning Inference Engineer) Craig Wittenbrink (NVIDIA, Senior Director, TensorRT) Kismat Singh (NVIDIA,) Ashwin Nanjappa (NVIDIA,) Trevor Morris (NVIDIA, Deep Learning Software Engineer) Dong Meng (NVIDIA,) Pooya Davoodi (NVIDIA, Senior Software Engineer) Po-Han Huang (NVIDIA,) *SJCC Hall 3 Pod A (Concourse Level)*
Practical Machine	Leveraging GPUs for	Redefining Robots

Learning Interpretability Techniques Navdeep Gill (H2O.ai, Senior Machine Learning/Software Engineer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Environmental Omics Analysis Kjiersten Fagnan (Joint Genome Institute, Lawrence Berkeley National Laboratory, CIO) *SJCC Room 211B (Concourse Level)*	Demystify Next Generation AI Enabled Robotics Bastiane Huang (Osaro, Product Manager) *SJCC Room 210C (Concourse Level)*
Connect with the Experts: Investigations into making GPU I/O Scream on Platforms of Today and Tomorrow CJ Newburn (NVIDIA, Principal Architect for HPC, NVIDIA Compute Software) Sandeep Joshi (NVIDIA, Manager, File systems and Performance) Kiran Kumar Modukuri (NVIDIA, Senior Software Engineer) *SJCC Hall 3 Pod C (Concourse Level)*	Connect with the Experts: Data Analytics on GPU: Algorithms and Implementations Nikolay Sakharnykh (NVIDIA, Sr. Developer Technology Engineer) Jake Hemstad (NVIDIA, Developer Technology Engineer) Murat Guney (NVIDIA, Developer Technology Engineer) Tim Kaldwey (NVIDIA, Manager DevTech AI) Jiri Kraus (NVIDIA, Senior Devtech Compute) *SJCC Hall 3 Pod D (Concourse Level)*	Connect with the Experts: OpenACC Andreas Herten (Jülich Supercomputing Centre, Researcher GPUs in High Performance Computing) Jeff Larkin (NVIDIA, Senior DevTech Software Engineer) Guido Juckeland (Helmholtz-Zentrum Dresden-Rossendorf, Head of Computational Science Department) Sunita Chandrasekaran (University of Delaware, Assistant Professor) Michael Wolfe (NVIDIA, Compiler Engineer) Mathew Colgrove (NVIDIA, PGI Devtech Engineer) *SJCC Hall 3 Pod E (Concourse Level)*
6 Perspectives: The Impact of AI Workloads on Data Center Compute and Memory (Presented by Samsung) Marc Tremblay (Microsoft, Distinguished Engineer) Sumit Gupta (IBM, VP, AI, Machine Learning & HPC) Anand Iyer (Samsung Semiconductor, Inc., Director, Product Planning) Rob Ober (NVIDIA, Chief Platform Architect, Datacenter Products) Cliff Young (Google, Software Engineer) Greg Diamos (Baidu, Research Lead) Maxim Naumov (Facebook, Research Scientist) *Marriott Hotel Ballroom 3 (Level 2/Concourse)*	Microsoft Azure: GPUs for Visualization, AI and HPC (Presented by Microsoft) Ian Finder (Microsoft, Senior PM Manager) *Hilton Hotel Santa Clara Room (Level 2/Concourse)*	Accelerated Computing Solutions for AI and HPC Workloads (Presented by Super Micro) Sarosh Irani (Super Micro Computer, Inc., Sr. Director of Product Management, GPU Systems & DL Solutions) *Marriott Hotel Ballroom 2 (Level 2/Concourse)*
NVIDIA Quadro Advanced Display Features	Connect with the Experts: Performance Analysis and	

	Ian Williams (NVIDIA,) Rupali Deshpande (NVIDIA,) Steve Nash (NVIDIA, Applied Engineer) *SJCC Hall 3 Pod B (Concourse Level)*	Optimization Kamesh Arumugam (NVIDIA,) Moises Hernandez (NVIDIA, -) Peng Wang (NVIDIA,) *SJCC Hall 3 Pod F (Concourse Level)*	
12:00PM - 02:00PM *SJCC South Hall*	**Lunch**		
12:00PM - 02:00PM Sessions	Connect with the Experts: Building a Self-Driving Car with NVIDIA DRIVE AGX Jeffrey Hetherly (NVIDIA, Sr. Deep Learning Software Engineer) Jay Chen (NVIDIA, Solution Architect) Kamal Kannan Balagopalan (NVIDIA,) Dennis Lui (NVIDIA, Senior Solution Architect) Aaraadhya Narra (NVIDIA, Solutions Architect) Nigel Star (NVIDIA,) Miguel Sainz (NVIDIA,) *SJCC Hall 3 Pod A (Concourse Level)*	Connect with the Experts: PGI Fortran, C and C++ Compilers & Tools Mathew Colgrove (NVIDIA, PGI Devtech Engineer) Michael Wolfe (NVIDIA, Compiler Engineer) *SJCC Hall 3 Pod E (Concourse Level)*	Connect with the Experts: The Making of Grand Format Human-AI Hybrid Art Chris Lamb (NVIDIA, Vice President of Computing Software) Joseph Smarr (Google, Senior Staff Software Engineer) Daniel Ambrosi (dreamscapes.ai, Human-AI Hybrid Artist) *SJCC Hall 3 Pod C (Concourse Level)*
	Connect with the Experts: Jetson Embedded Platform Experts Zheng Liu (NVIDIA,) Felix Schmitt (NVIDIA,) Eric Work (NVIDIA,) Winnie Hsu (NVIDIA,) Dipen Patel (NVIDIA,) Eric Brower (NVIDIA, Senior Director, Linux Platform Software) Kismat Singh (NVIDIA,) Sanjiv Satoor (NVIDIA, Sr. Engineering Manager, Developer Tools) Sarah Adams (NVIDIA,) Sherif Eid (NVIDIA,) Amulya Yarlagadda (NVIDIA,) Abeed Salam (NVIDIA,) Shane Chen (NVIDIA,) Vincent Nguyen (NVIDIA, DLI Certified Instructor) Martin Doe (NVIDIA,) Patricia Li (NVIDIA,) John Hsu (NVIDIA,) Bridget Bissell (NVIDIA,) Phil Lawrence (NVIDIA,)	Connect with the Experts: VirtualGPU SA experts Jeremy Main (NVIDIA,) Erik Bohnhorst (NVIDIA, Professional Visualization Performance Engineering Manager) Shailesh Deshmukh (NVIDIA, Sr. Solutions Architect) *SJCC Hall 3 Pod D (Concourse Level)*	

	Daniel Horowitz (NVIDIA, Director of Engineering in Developer Tools) Jing Yang (NVIDIA,) Anisha Goel (NVIDIA,) Aayush Rajoria (NVIDIA, System Software Engineer) Nicolas Droux (NVIDIA,) Vincent Chung (NVIDIA,) Sean Pieper (NVIDIA,) Ashok Kelur (NVIDIA, Senior System Software Engineer) Jason Su (NVIDIA,) Stuart Yates (NVIDIA,) Prabhu Kuttiyam (NVIDIA,) *SJCC Hall 3 Pod B (Concourse Level)*		
02:00PM - 03:00PM Sessions	Composable Infrastructure for On-Prem Kubernetes-Based Systems Subrahmanyam Ongole (One Convergence, Architect) *SJCC Room 212B (Concourse Level)*	Packet Processing on GPU at 100GbE Line Rate Chetan Tekur (NVIDIA, FAE) Elena Agostini (NVIDIA, Software Engineer) *SJCC Room 211B (Concourse Level)*	Object Detection for Autonomous Machines Rohit Taneja (NVIDIA, Solutions Architect) *SJCC Room 220C (Concourse Level)*
	Building and Optimizing Cloud Platform for Audio Cognition System Yoonchang Han (Cochlear.ai, CEO) Subin Lee (Cochlear.ai, COO) *SJCC Room 210A (Concourse Level)*	Police Patrol Optimization with Geospatial Deep Reinforcement Learning Daniel Wilson (Esri, Senior Data Scientist) *SJCC Room 210E (Concourse Level)*	Can FPGAs compete with GPUs? Bram Veenboer (ASTRON (Netherlands Institute for Radio Astronomy), Researcher) John Romein (ASTRON (Netherlands Institute for Radio Astronomy), Senior Researcher) *SJCC Room 211A (Concourse Level)*
	Design Decisions with Large AEC Scenes and Advanced Realistic Materials in VR Javier Glatt (CADMakers, Co-Founder & CEO) Stephan Ritz (Dassault Systemes, CATIA Design, Product Experience | Portfolio Management Director) *SJCC Room 230A (Concourse Level)*	Scaling Up Deep Learning for Autonomous Driving Jose M. Alvarez (NVIDIA, Senior DL Engineer) *SJCC Room 220A (Concourse Level)*	Taming the Hydra: Multi-GPU Programming with OpenACC Jeff Larkin (NVIDIA, Senior DevTech Software Engineer) *SJCC Room 210F (Concourse Level)*
	Volta & Turing: Architecture and Performance optimization	A New Direct Connected Component Labeling and Analysis Algorithm for	Dask and V100s for Fast, Distributed Batch Scoring of Computer Vision

	Guillaume Thomas-Collignon (NVIDIA, Devtech Engineer) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	GPUs Arthur Hennequin (CERN, Ph.D.) Lionel Lacassagne (Sorbonne University - LIP6 laboratory, Professor in Computer Architecture) *SJCC Room 210C (Concourse Level)*	Workloads Mathew Salvaris (Microsoft, Senior Data Scientist) Danielle Dean (Microsoft, Principal Data Science Lead) *SJCC Room 210D (Concourse Level)*
	Industrial automation based on auto ML and GANs to address predictive maintenance Vladislav Mironov (Conundrum, CTO and Co-founder) Konstantin Kiselev (Conundrum, CEO) *SJCC Room 210B (Concourse Level)*	Machine Learning for Security and Security for Machine Learning Nicole Nichols (Pacific Northwest National Lab, Senior Research Scientist) *SJCC Room 212A (Concourse Level)*	Corporate Tools for GPU Access and Software Development Alex Gartner (The MITRE Corporation, Senior Software Systems Engineer) Christine Harvey (The MITRE Corporation, Lead High Performance & Analytic Computing Engineer) *SJCC Room 210G (Concourse Level)*
	A Fast Forward through "Ray Tracing Gems" Eric Haines (NVIDIA, Distinguished Engineer) *SJCC Room 230B (Concourse Level)*		
03:00PM - 05:00PM Sessions	HPC-Powered Workflows for Diagnostics, Therapeutics, and Patient Care Andrea Borsic (NE Scientific LLC, CEO & Founder) Andras Wirth (Mediso Ltd., SPECT Modality Department Leader) Hammad Omer (COMSATS University Islamabad, Pakistan, Assistant Professor) Tian Liu (NE Scientific LLC, Data Scientist) *SJCC Room 220B (Concourse Level)*		
03:00PM - 04:00PM Sessions	Improving GPU Utilization for Multi-tenant Deep Learning Workloads on DGX-2 and Public GPU Clouds Jeongkyu Shin (Lablup Inc., Chief Executive Officer)	CNN Inference with cuDNN: Common Pitfalls and Best Practices Sven Middelberg (NVIDIA, Senior Developer Technology Engineer) *SJCC Room 210A (Concourse Level)*	Filling the Performance Gap in Convolution Implementations for NVIDIA GPUs Antonio J. Peña (Barcelona Supercomputing Center (BSC), Sr. Researcher)

	Joongi Kim (Lablup Inc., Chief Technology Officer) *SJCC Room 212B (Concourse Level)*		Pedro Valero-Lara (Barcelona Supercomputing Center (BSC), Research Associate) *SJCC Room 210E (Concourse Level)*
	Developing High-Quality Multilingual Sentence Encoders Gregor Stewart (Medallia, VP Data Science) Andrew Yeager (Medallia, Inc., Staff Data Scientist) *SJCC Room 210D (Concourse Level)*	Understanding Genome Regulation with Interpretable Deep Learning Avanti Shrikumar (Stanford University, PhD Student) *SJCC Room 211B (Concourse Level)*	Refactoring the Unreal Engine Rendering Code Base Marcus Wassmer (Epic Games, Lead Rendering Programmer) *SJCC Room 230B (Concourse Level)*
	Deploying Robotaxis Around the World Anton Slesarev (Yandex Taxi, Head of Engineering for Autonomous Driving) Pavel Vorobev (Yandex Taxi, Head of Product for Autonomous Driving) *SJCC Room 220A (Concourse Level)*	Tensor Core Performance: The Ultimate Guide Michael Andersch (NVIDIA, GPU Architect) *Marriott Hotel Ballroom 4 (Level 2/Concourse)*	Multi-Scale Simulations on Sierra with Volta GPUs and the Legion Programming System Galen Shipman (LANL, Scientist) Michael Bauer (NVIDIA, Senior Research Scientist) *SJCC Room 210G (Concourse Level)*
	Learning Viewpoint Estimators with Few Examples Shalini De Mello (NVIDIA, Senior Research Scientist) Hung-Yu Tseng (UT Merced, Research Intern, PhD Student) *SJCC Room 210C (Concourse Level)*	Solving Logistics Problems with Deep RL Karim Beguir (InstaDeep, Co-Founder & CEO) *SJCC Room 210B (Concourse Level)*	New FFT Library with Flexible C++ API Lukasz Ligowski (NVIDIA, Senior Systems Engineer) *SJCC Room 210F (Concourse Level)*
	Detecting The Unknown: Using Unsupervised Behavior Models To Expose Malicious Network Activity Aaron Sant-Miller (Booz Allen Hamilton, Lead Data Scientist) *SJCC Room 212A (Concourse Level)*	Unified Memory for Data Analytics and Deep Learning Nikolay Sakharnykh (NVIDIA, Sr. Developer Technology Engineer) Chirayu Garg (NVIDIA, AI Developer Technology Engineer) *SJCC Room 211A (Concourse Level)*	Accessible MR Glasses for Gaming and Enterprise Chi Xu (nreal, CEO) *SJCC Room 230A (Concourse Level)*
04:00PM - 05:00PM Sessions	How To Build Efficient ML Pipelines From the Startup Perspective Jaeman An (AITRICS, Senior Software Engineer) *SJCC Room 212B (Concourse Level)*	Perlmutter- A 2020 Pre-Exascale GPU-accelerated System for NERSC: Architecture and Application Performance Optimization Nick Wright (Lawrence Berkeley Lab, NERSC,	Real-Time Ray Tracing in Unreal Engine Juan Canada (Epic Games, Lead Ray Tracing Programmer) *SJCC Room 230B (Concourse Level)*

		Group Lead Advanced Technologies Group) *SJCC Room 211A (Concourse Level)*	
	Transfer Learning-Based GPU-Accelerated Deep Learning for End-to-End Industrial Inspection Andrew Liu (NVIDIA, Sr. Solution Architect) Peter Pyun (NVIDIA, Principal Solution Architect) *SJCC Room 210B (Concourse Level)*	Path Tracing in ParaView-OptiX: RTX for Scientific Visualization Tim Biedert (NVIDIA, Senior Scientific Visualization Developer Technology Engineer) Mathias Hummel (NVIDIA, Senior Scientific Visualization Developer Technology Engineer) *SJCC Room 211B (Concourse Level)*	Materials Discovery with Artificial Intelligence Hyo Sug Lee (Samsung Advanced Institute of Technology, Corporate VP/Head of Lab) Youn-Suk Choi (Samsung Advanced Institute of Technology, Principal Researcher/Research Master) *SJCC Room 210G (Concourse Level)*
	Euclidean Distance Transform on Xavier Stanley Tzeng (NVIDIA, Senior Architect) *SJCC Room 210C (Concourse Level)*	Accelerating Graph Algorithms with RAPIDS Joe Eaton (NVIDIA, Technical Lead for Data and Graph Analytics) *SJCC Room 212A (Concourse Level)*	Landing the RoboTaxi Service Yan Li (WeRide, Co-founder & CTO) *SJCC Room 220A (Concourse Level)*